

EV Dx34A Digital Parameters

Dx34A Software Rev.: 3.01

File: PI XXX Systems Dx34 Parameters Rev 1.1

Loudspeaker System		MH/PI940C (PI9415, MH940C, PI940C & PI115L)					MH/PI660C (PI6615, MH660C, PI660C & PI115L)				
Notes Un-hide cells for revision history and specific system notes.		* Adjust output levels and limiters as necessary									
Programmer: 1st Rev. - Last Rev.		DWG-1/30/95 DEC-4/26/99					DWG-1/30/95 DEC-4/26/99				
Dx34A Program Title		MH/PI940C					MH/PI660C				
Dx34A Configuration		3-Way					3-Way				
Frequency Band		FR	LF	MB	HF		FR	LF	MB	HF	
Dx34A Output		1,2&3	1	2	3	4	1,2&3	1	2	3	4
Edit Menu	Input Master Delay (mS)	2.0					2.0				
	Input Master PEQ Freq (Hz)										
	Input Master PEQ Q (Q)										
	Input Master PEQ Gain (dB)										
	Low-Cut Freq (Hz)		48.0					48.0			
	Low-Cut Slope (dB/Oct)		12					12			
	Low-Cut Q (Q)		1.0					1.0			
	LSF Freq. (Hz)		180					180			
	LSF Slope (dB/Oct)		6					6			
	LSF Gain (dB)		+9.0					+9.0			
	HPF Freq. (Hz)			140	1480				180	1480	
	HPF Resp. (Type-dB/Oct)			LR24	BW24				LR24	BW24	
	PEQ1 Freq. (Hz)		80.0	840	2100			80.0	148	2000	
	PEQ1 Q (Q)		1.0	1.8	2.4			1.0	5.2	1.3	
	PEQ1 Gain (dB)		0	+11.0	-6.0			0	+9.0	-6.0	
	PEQ2 Freq. (Hz)			760	16000				680	14800	
	PEQ2 Q (Q)			4.6	2.6				3.0	1.6	
	PEQ2 Gain (dB)			-12	+12.0				-2.0	+10.0	
	LPF Freq. (Hz)		160	1240				160	1320		
	LPF Resp. (Type-dB/Oct)		LR24	BW24				LR24	LR24		
HSF Freq. (Hz)				10000					7000		
HSF Slope (dB/Oct)				12					12		
HSF Gain (dB)				+6.0					+5.0		
Output Align Delay (uS)		1515	0	1237			1515	0	1408		
Polarity (Normal, Invert)		Norm	Norm	Norm			Norm	Norm	Norm		
Digital Output Gain (dB)		+2.0	+1.0	-2.0			+2.0	+1.0	-2.0		
Limiter Thresh. (dBu)		21	21	21			21	21	21		
Limiter Decay (dB/mS)		50	50	50			50	50	50		
Limiter Hold (mS)		0	0	0			0	0	0		
Channel 1 Mode (L,R,L+R)		L					L				
Channel 4 Mode (L,R,L+R)					L+R					L+R	
Knob	Output Knobs (dB)		0	0	0	0		0	0	0	0
	Input Knob (dB)	0					0				
Options	2-Way L-R Mode										
	Delay Units	uSec					uSec				
	Limiter Thresh. Reference	dBu (0dBu=.775v)					dBu (0dBu=.775v)				
	VU Display	No Peak (dB from clip)					No Peak (dB from clip)				

EV Dx34A Digital Parameters

Dx34A Software Rev.: 3.01

File: PI XXX Systems Dx34 Parameters Rev 1.1

Loudspeaker System		MH/PI640C (PI6415, MH640C, PI640C & PI115L)									
Notes Un-hide cells for revision history and specific system notes.		* Adjust output levels and limiters as necessary									
Programmer: 1st Rev. - Last Rev.		DWG-1/30/95 DEC-4/26/99									
Dx34A Program Title		MH/PI640C									
Dx34A Configuration		3-Way					3-Way				
Frequency Band		FR	LF	MB	HF		FR				
Dx34A Output		1,2&3	1	2	3	4	1,2&3	1	2	3	4
Edit Menu	Input Master Delay (mS)	2.0					2.0				
	Input Master PEQ Freq (Hz)										
	Input Master PEQ Q (Q)										
	Input Master PEQ Gain (dB)										
	Low-Cut Freq (Hz)		48.0								
	Low-Cut Slope (dB/Oct)		12								
	Low-Cut Q (Q)		1.0								
	LSF Freq. (Hz)		180								
	LSF Slope (dB/Oct)		6								
	LSF Gain (dB)		+9.0								
	HPF Freq. (Hz)			160	1200						
	HPF Resp. (Type-dB/Oct)			LR24	BW24						
	PEQ1 Freq. (Hz)		80.0	148	1840						
	PEQ1 Q (Q)		1.0	5.2	2.2						
	PEQ1 Gain (dB)		0	+9.0	-6.0						
	PEQ2 Freq. (Hz)			490	16000						
	PEQ2 Q (Q)			4.0	2.6						
	PEQ2 Gain (dB)			+2.0	+12.0						
	LPF Freq. (Hz)		160	1280							
	LPF Resp. (Type-dB/Oct)		LR24	BW24							
HSF Freq. (Hz)				10000							
HSF Slope (dB/Oct)				12							
HSF Gain (dB)				4.0							
Output Align Delay (uS)		1515	0	1237							
Polarity (Normal, Invert)		Norm	Norm	Norm							
Digital Output Gain (dB)		+2.0	0	-4.0							
Limiter Thresh. (dBu)		21	21	21							
Limiter Decay (dB/mS)		50	50	50							
Limiter Hold (mS)		0	0	0							
Channel 1 Mode (L,R,L+R)		L									
Channel 4 Mode (L,R,L+R)					L+R						
Knob	Output Knobs (dB)		0	0	0	0		0	0	0	0
	Input Knob (dB)	0					0				
Options	2-Way L-R Mode										
	Delay Units	uSec					uSec				
	Limiter Thresh. Reference	dBu (0dBu=.775v)					dBu (0dBu=.775v)				
	VU Display	No Peak (dB from clip)					No Peak (dB from clip)				