

III. SELECTABLE OPTIONS

TARG has several switch selectable options which are controlled by a 8-position DIP switch located on the main logic board at position 16A. This switch is accessible from the front of the game through the coin door. Figure 1 shows all selectable functions controlled by the 8-position DIP switch.

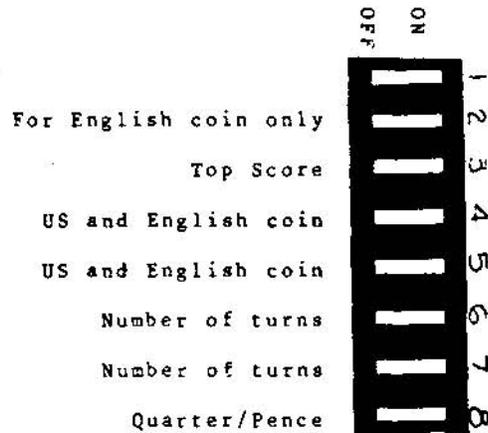
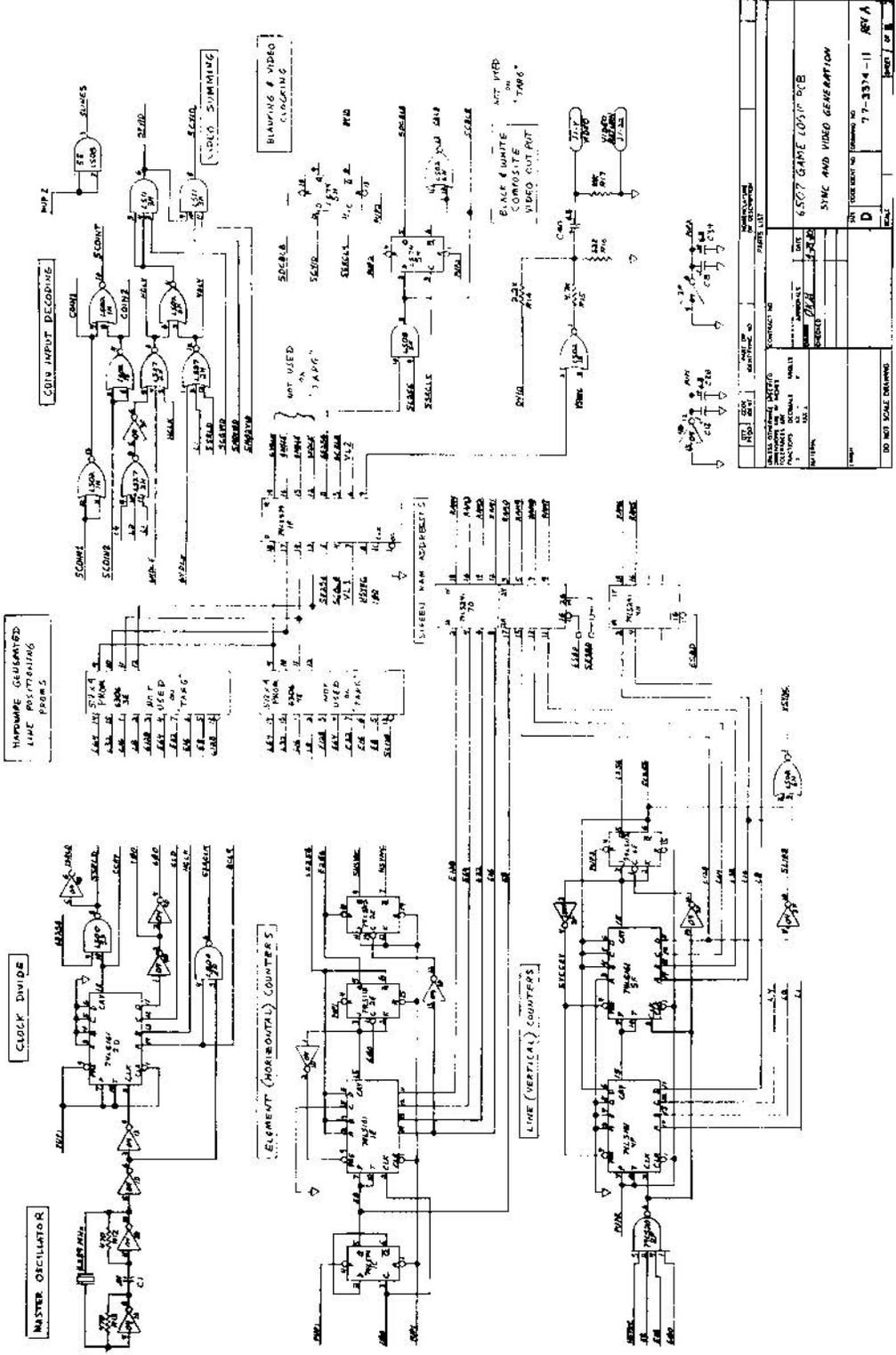


Figure 6.5: Functions of the 8-position DIP Switch

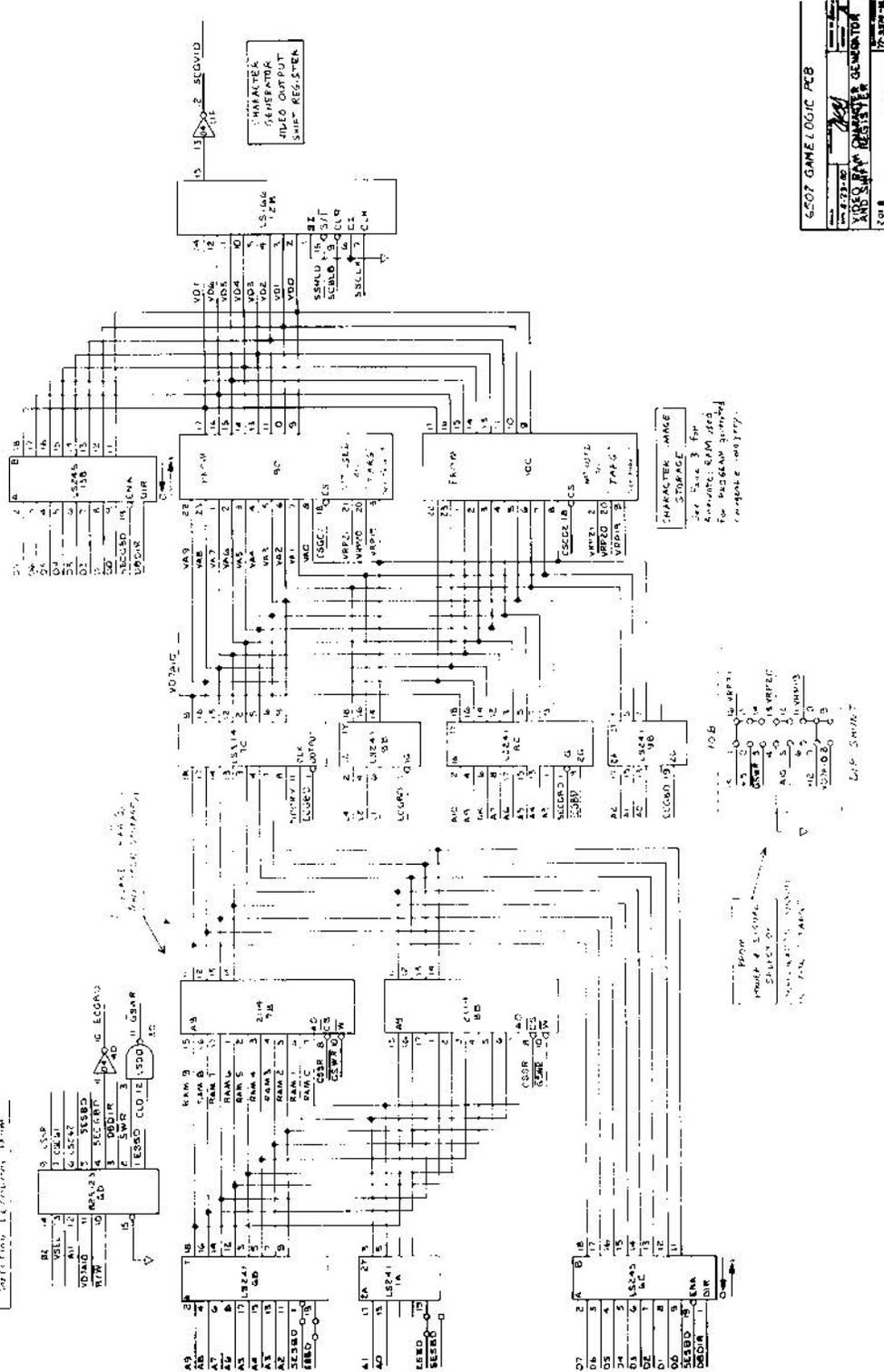
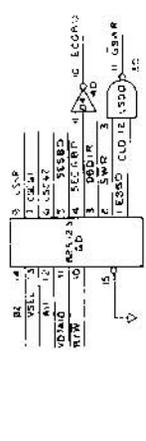
Following are the switch settings for the selection of options:

A. COINAGE	Switch 4	Switch 5		
2 Player - 1 Coin	OFF	OFF		
1 Player - 1 Coin	OFF	ON		
2 Player - 2 Coin				
1 Player - 2 Coin	ON	OFF		
2 Player - 4 Coin				
B. TYPE OF COINS	Switch 8			
Quarters	OFF			
English coins	ON			
C. FOR ENGLISH COINS ONLY		Switch 2	Switch 4	Switch 5
1 game, one 10 pence coin		ON	OFF	OFF
6 games one 50 pence coin				
1 game two 10 pence coins		OFF	OFF	OFF
3 games one 50 pence coin				
D. NUMBER OF TURNS	Switch 6	Switch 7		
2 Wummels (turns)	OFF	OFF		
3 Wummels	ON	OFF		
4 Wummels	OFF	ON		
5 Wummels	ON	ON		
E. TOP THIS SCORE	Switch 3			
Credit awarded for topping score	ON			
Extended play for topping score	OFF			



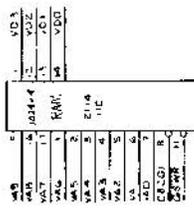
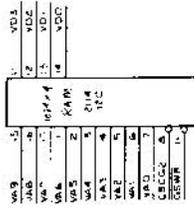
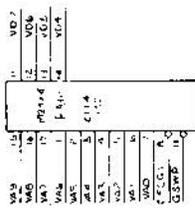
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REVISED	CHECKED BY	VIDEO GENERATION
APPROVED	DATE	
PROJECT	NO.	
DO NOT SCALE DRAWING		
SYNCHRONIZATION		
PARTS LIST		
CONTRACT NO.		
6507 GAME LOGIC PCB		
VIDEO GENERATION		
71-3314-11 REV A		
PAGE 1 OF 1		

SCREEN ON SELECT AND KEY
 POSITION LOCKING SYSTEM

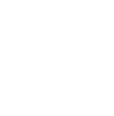
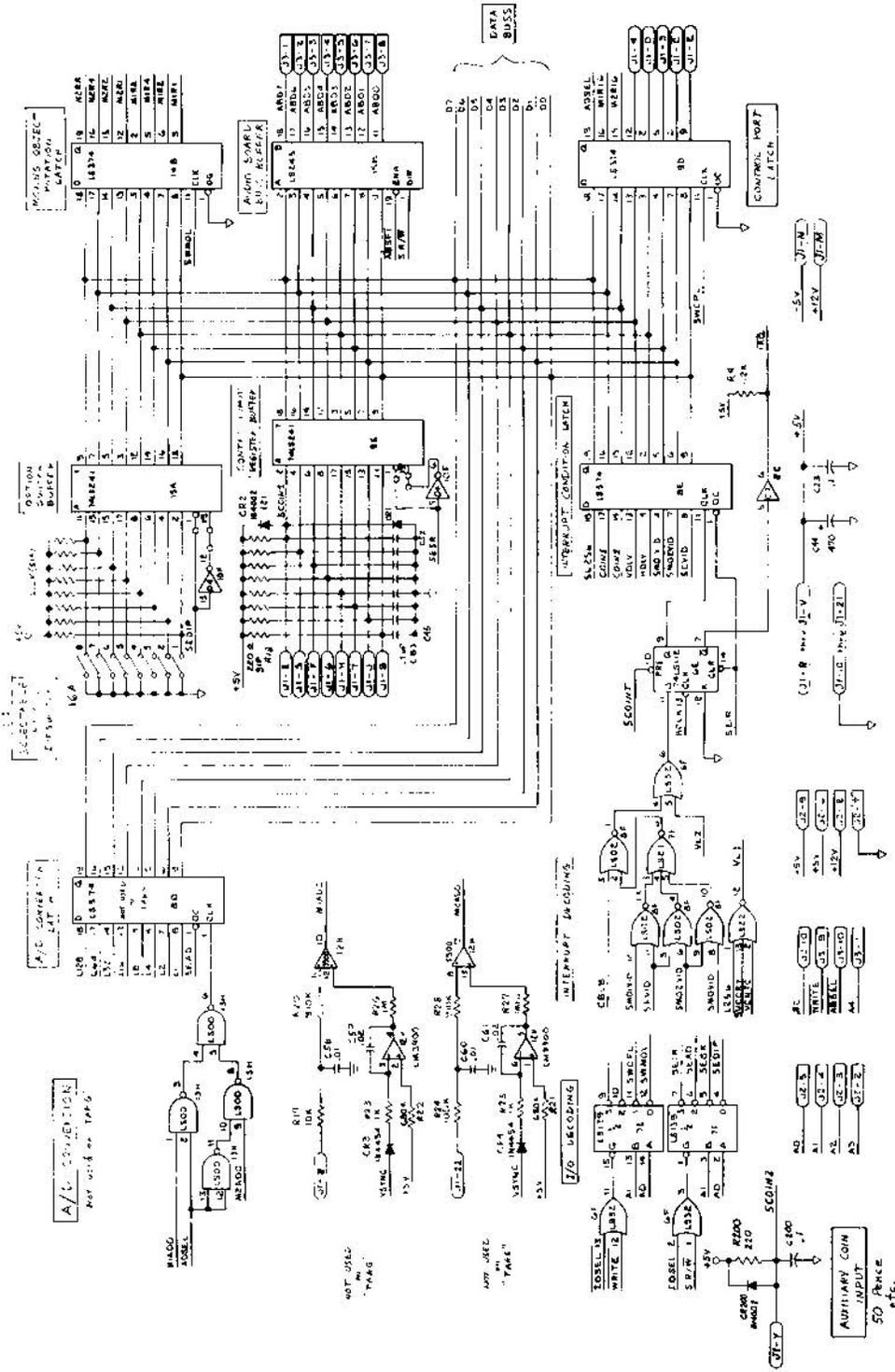


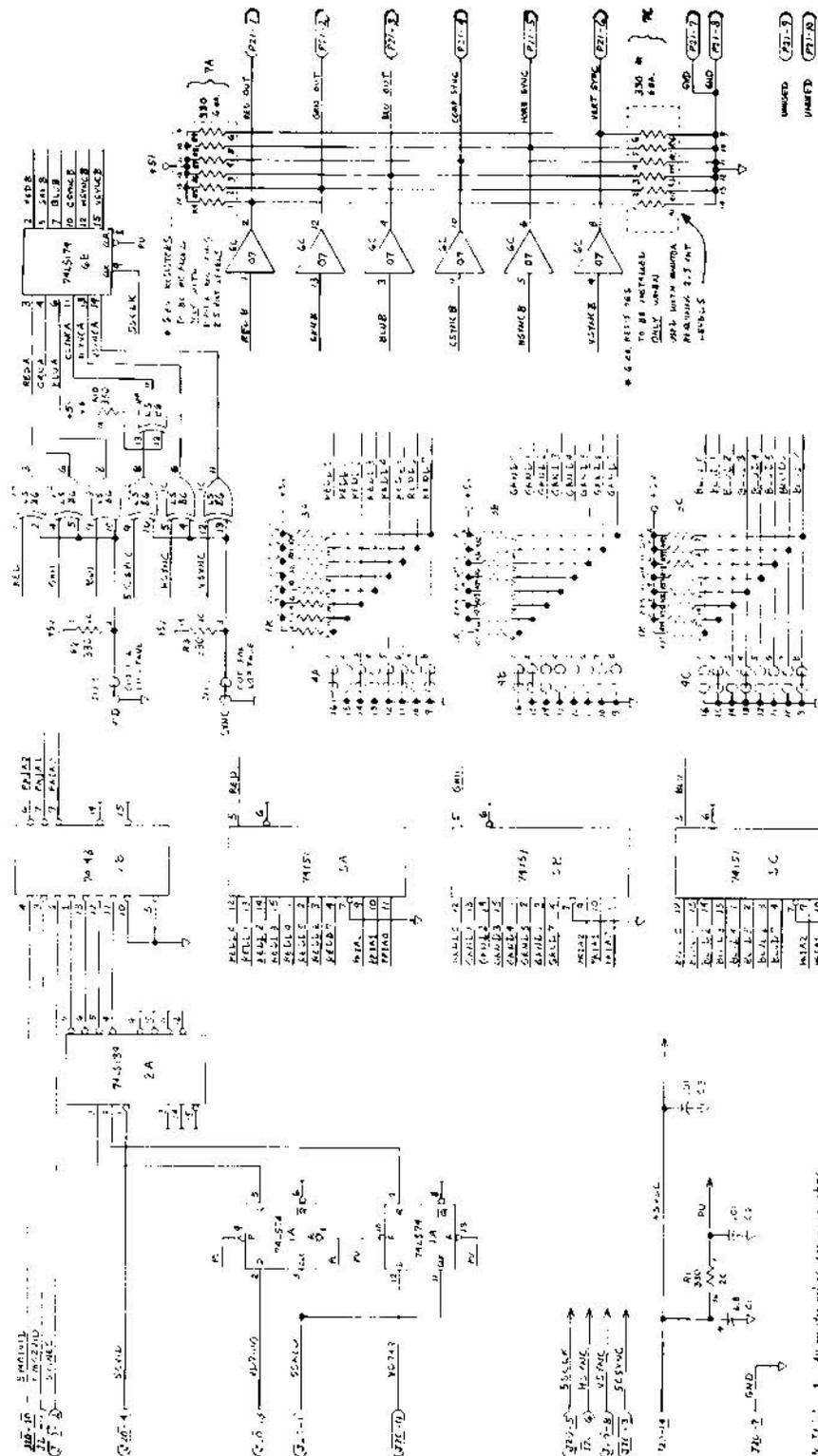
6507 GAME LOGIC PCB
 4-73-00
 VIDEO RAM REGISTER GENERATOR
 17-3374-00

1. ANALOG CONTROLLER
 2. IMAGE STORAGE RAM
 For J2C with program
 attached to image controller
 3. J2C



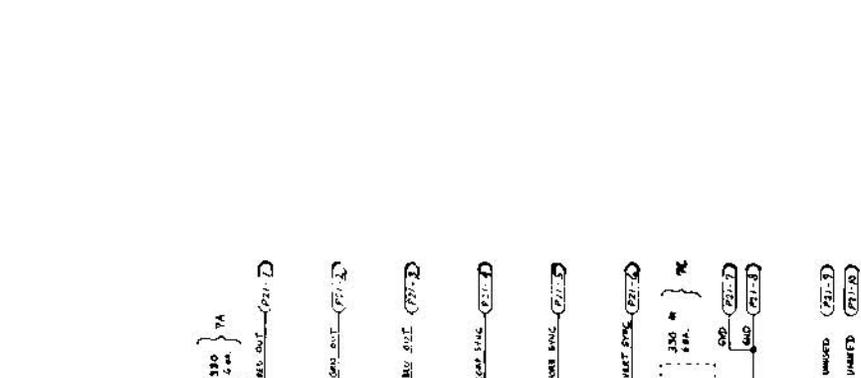
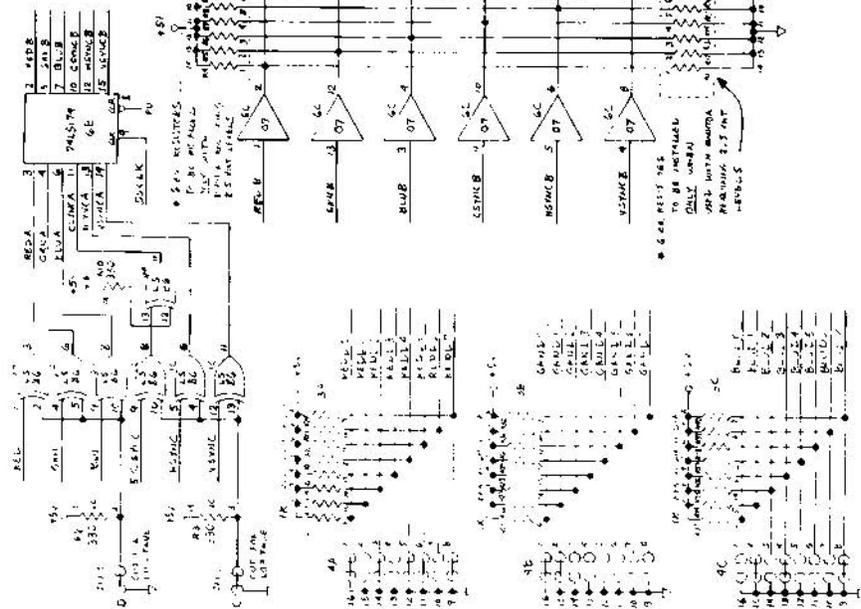
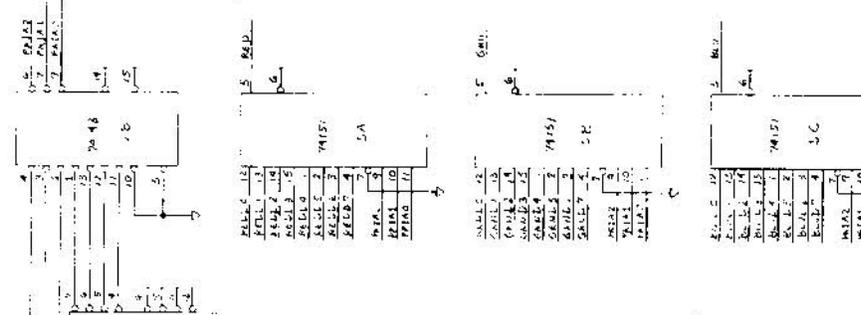
6502 GAME LOGIC PCB
 IMAGE STORAGE RAM
 1. J2C

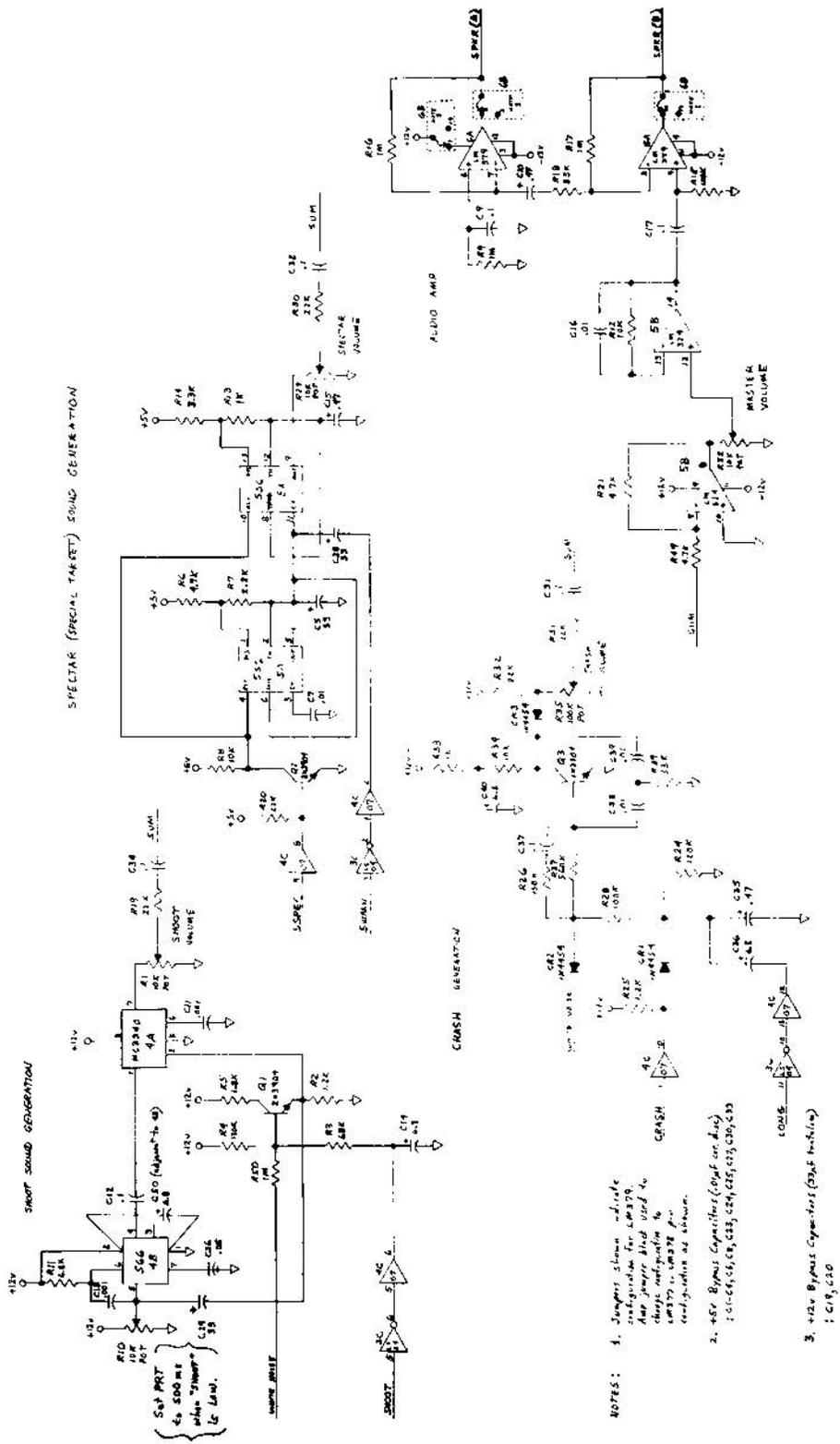




UNTESTED (P21-5)
UNTESTED (P21-10)

1. All resistor values shown in Ohms
2. All logic for values shown in microseconds
3. C4-C6 (not shown) are .01 uF cap. @ 50V





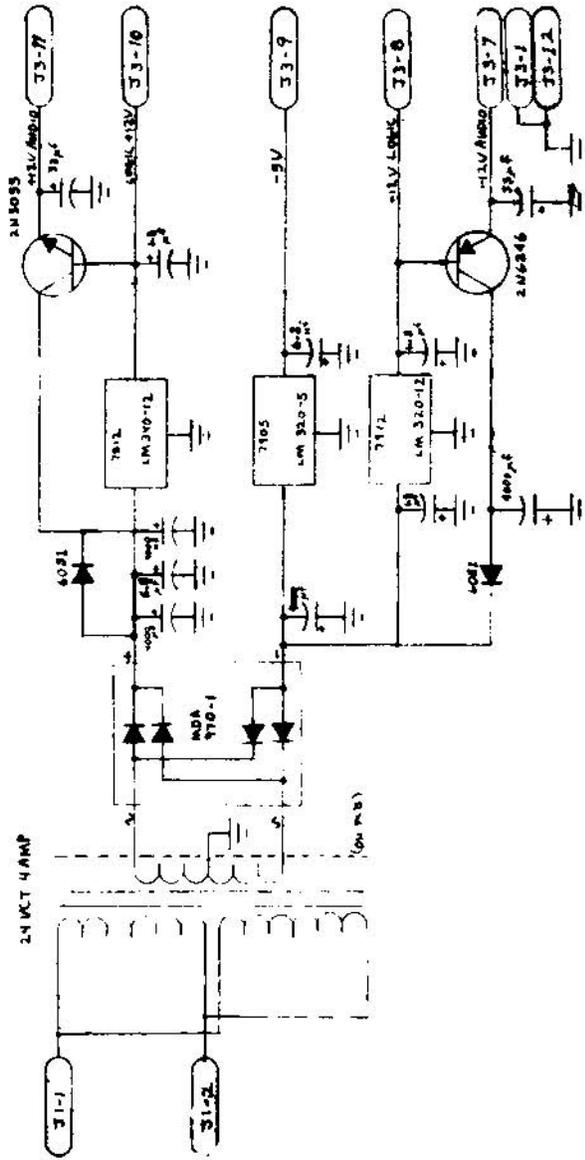
SPECTAR (SPECIAL TARGET) SOUND GENERATION

SHOOT SOUND GENERATION

CRASH GENERATION

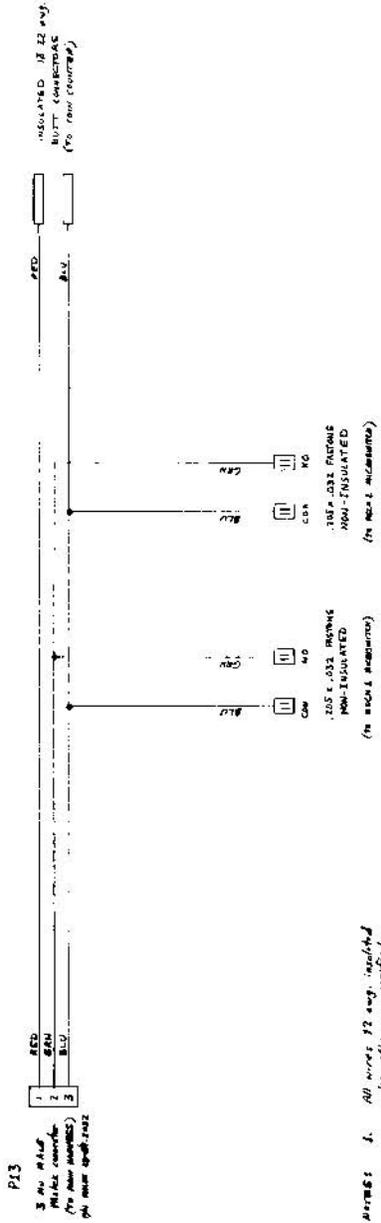
AUDIO AMP

- NOTES:
1. Jumpers shown indicate modifications for AM379. Amp power also used to change configuration to AM379 is AM379 pin configuration as shown.
 2. 45V Bypass Capacitors (only on 45V) : C11, C15, C18, C23, C24, C25, C26, C30, C33
 3. 12V Bypass Capacitors (50µF preferred) : C10, C20



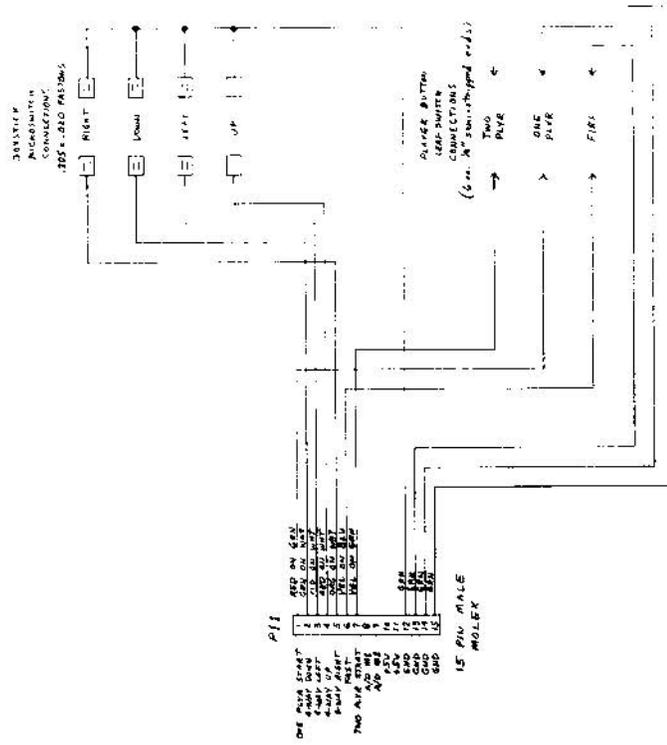
UNIVERSAL POWER SUPPLY SCHEMATIC
 +12 V Amps @ Reg., -12 V Reg. @ Reg.
 -5 V Reg.

TARG
CPU HARNESS SCHEMATIC
(UNBROKEN LOW
VOLTAGE)



- NOTES: 1. ALL WIRE 18 AWG. INSULATED
UNLESS OTHERWISE SPECIFIED.
2. ABSOLUTELY NO SUBSTITUTIONS
ON SPECIFIED WIRE COLORS.

TARG
CONTROL PANEL HARNESS
SCHEMATIC



- NOTES:
1. All wires to be installed with correct polarity.
 2. ABSOLUTELY NO SUBSTITUTION ON SPECIFIED WIRE COLORS