

MOUSE TRAP™

Operation and Service
Manual
3rd Edition



MOUSE TRAP™

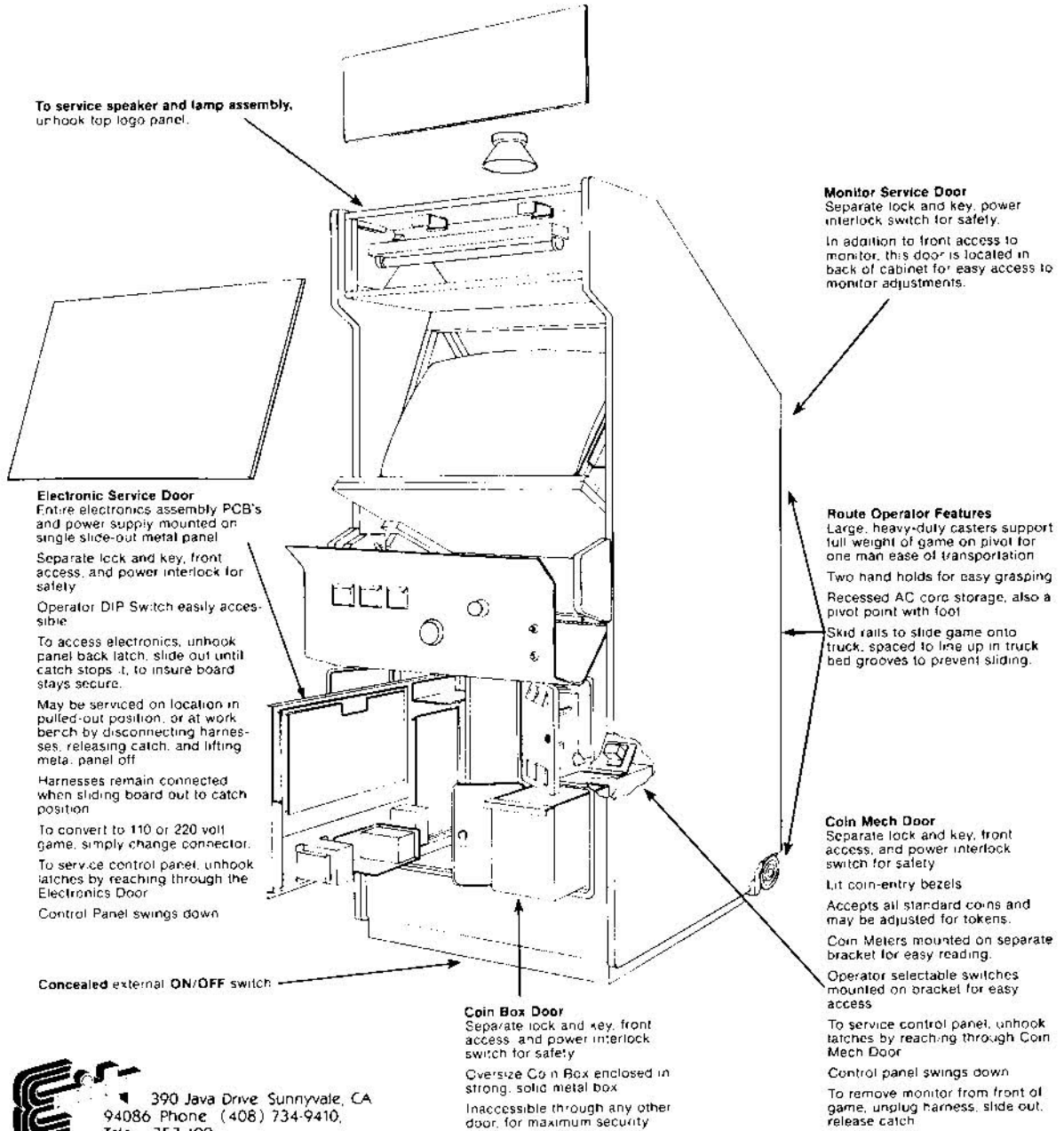
Operation and Service Manual
3rd Edition

(c) 1981 Exidy, Inc.
390 Java Drive, Sunnyvale, California 94086-1271
Telephone: (408) 734-9410
Toll-free: (800) 538-8402
Telex: 357-499

INTRODUCING THE EXIDY OPERATOR CONVENIENCE PACKAGE

Exidy's New Operator Convenience Package makes all components accessible through the front of the game. Each door has its own separate

lock and key securing game service to **capable** hands, and **profits** to the **right** hands!



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2.0 ADJUSTMENTS

2.1 POWER SUPPLY ADJUSTMENTS

All DC Power required to operate MOUSE TRAP™ is supplied by the Exidy Power Supply Module.

CAUTION: Only certified technicians should make adjustments on all components of MOUSE TRAP™. AC line voltage selection is available in your MOUSE TRAP™ game by setting the appropriate jumpers on the power chassis. Only the +5v DC is adjustable. This must be adjusted to:

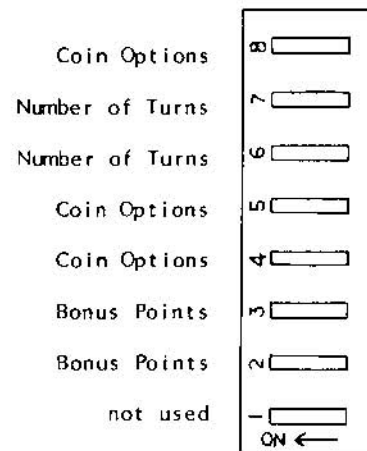
+5.00v DC +/- .25v

as measured on the Logic PCB near the microprocessor (location 2A).

2.2 SELECTABLE OPTIONS

MOUSE TRAP™ has several switch selectable options controlled by an 8-position DIP switch located on the main logic board at position 16A. This switch is accessible through the front electronics door. Simply slide the logic board out and locate the DIP switch on the lower left-hand corner.

The following drawing shows the particular functions controlled by the 8-position DIP switch.



Functions of the 8-position DIP Switch

2.3 SELECTABLE DIP SWITCH SETTINGS

MOUSE TRAP™ is shipped with the dip switch already set for optimum dollar return. Should you decide to change the settings, you may select any of the following options by setting the proper switch accordingly:

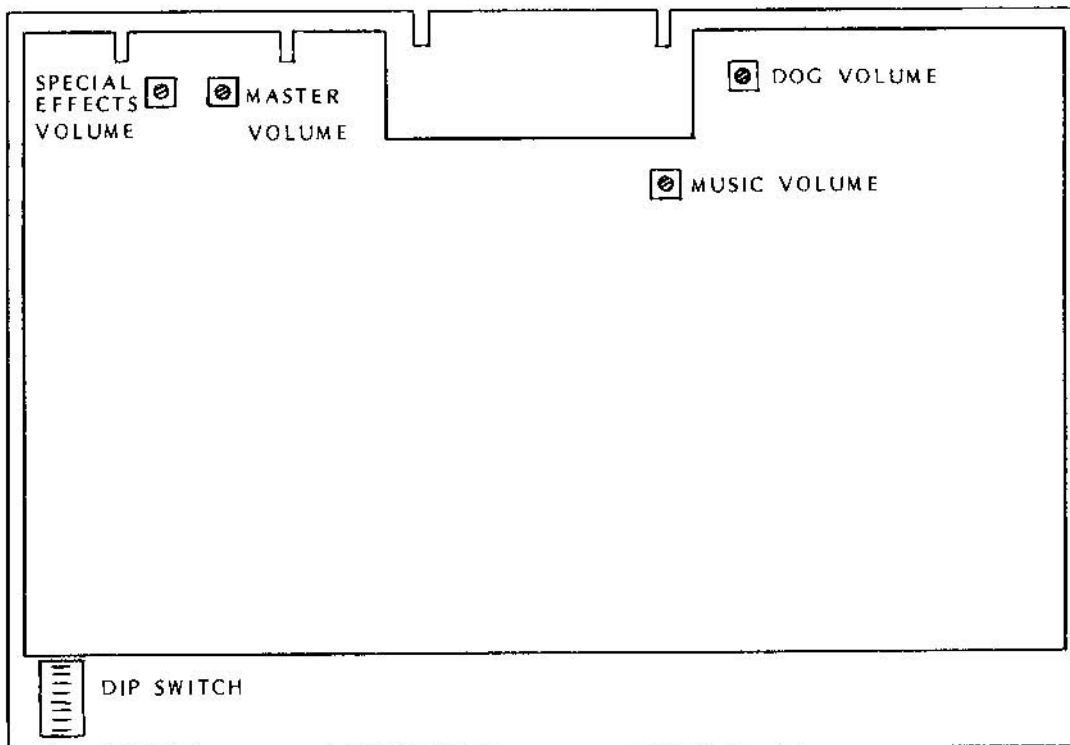
A. COINAGE	Switch 4	Switch 5	Switch 8
1 Coin - 1 Credit	OFF	OFF	OFF
2 Coins- 1 Credit	OFF	ON	OFF
1 Coin - 2 Credits	ON	OFF	OFF
1 Coin - 4 Credits	ON	ON	OFF
1 Coin - 3 Credits or 2 Coins- 7 Credits	OFF	ON	ON
1 Coin in Left Slot- 1 Credit 1 Coin in Right Slot- 5 Credits	OFF	OFF	ON
One Coin in Left Slot- 1 Credit 1 Coin in Right Slot- 4 Credits	ON	OFF	ON
Two Coins in Left Slot- 1 Credit 1 Coin in Right Slot- 3 Credits	ON	ON	ON

B. NUMBER OF TURNS	Switch 6	Switch 7
2 turns	OFF	OFF
3 turns	ON	OFF
4 turns	OFF	ON
5 turns	ON	ON

E. BONUS TURN (Extra turn awarded when selectable number of points are made).	Switch 2	Switch 3
Extra turn for 30,000 points	OFF	OFF
40,000	ON	OFF
50,000	OFF	ON
60,000	ON	ON

2.4 AUDIO BOARD ADJUSTMENTS

The illustration that follows points out the four audio adjustments and the location of the DIP switch. The audio board rides piggyback on the logic board, mounted on the left wall of the game, when viewed from the front service door.



3.0 SELF DIAGNOSTIC TESTS

MOUSE TRAP™ has two diagnostic test modes. The first mode, the Logic/Audio Diagnostic Test Mode, checks the RAM and ROM chips on the Logic Board, and simultaneously checks the Audio Board. This test mode is the normal self-test mode and is automatically performed when MOUSE TRAP™ is first turned on. The second mode tests the Controls and Color only. To run this test, activate the coin switch while powering up. We will explain both of these modes in full detail.

You may bypass both diagnostic modes by depressing either player one or player two start button while powering up. After 8 seconds of the message 'STAND BY VERSION X', (where X is a number) the Attract Mode appears.

3.1 LOGIC/AUDIO DIAGNOSTIC TEST MODE

The RAM Test

When MOUSE TRAP™ is first turned on, a processor and video RAM test is done. If the RAM passes, it goes immediately into the ROM test, without a message indicating it passed the RAM test.

If a RAM chip fails, an attempt is made to indicate the RAM chip where a failure was detected. Since the screen depends on a properly functioning RAM, this indication may not be displayed. A failed RAM may be indicated by a digit from 0 to 7 in every position on the screen using four colors. The code for these digits is as follows:

Number on Screen	RAM chip to check	
0	5A	Processor RAM
1	4A	failure
2	8B	Screen RAM
3	7B	failure
4	11C	
5	13C	Video RAM
6	12C	failure
7	14C	

The RAM test cycles if the failure is persistent.

The ROM Test

If the RAM test passes, the ROM test begins. The message STAND BY VERSION X appears on the screen (where X is a number). One by one, exclamation points (!) appear on the screen. Each exclamation mark indicates half (2K) of a ROM board chip, numbered 6A through 13A, has passed the diagnostic test. After all marks appear, the game then goes into the Attract Mode, indicating all is well.

If any failure is detected during the ROM test, a hex digit appears instead of an exclamation point with a 'BAD ROM' message at the top of the screen. The test repeats indefinitely if a bad ROM is encountered. The key for which chip to check is as follows:

Message: ROM Chip to check:

0	lower 2K of 11A
1	upper 2K of 11A
2	lower 2K of 10A
3	upper 2K of 10A
4	lower 2K of 9A
5	upper 2K of 9A
6	lower 2K of 8A
7	upper 2K of 8A
8	lower 2K of 7A
9	upper 2K of 7A
A	lower 2K of 6A
B	upper 2K of 6A

The Audio Test

While the Logic tests are underway, the Audio board is also being checked.

Five seconds after power on, one or more quick beeps, like an organ chord, are heard. This is part of the Exidy Audio Diagnostic Test. The number of beeps that sound indicate different conditions of the Audio board.

The following code is an indication **only**, of where to first check the Audio Board. Because this diagnostic test only evaluates certain components, other circuitry is relied upon for the test. Should this other circuitry fail, the diagnostic test may not, then, point directly to the failure. Please use the results of this test as a guideline for further troubleshooting.

The code is as follows:

- 0 beep: If no beeps are heard, along with a hum or random notes, this may indicate a failure in 3A and/or 7A.
- 1 beep: All audio hardware is OK. However, be sure to check the Attract Mode Cycle anyway for a possible message to check the Audio Board. In occasional instances, this can occur. The message will further direct you.
- 2 beeps: ZERO PAGE RAM failure. Check 6532 RAM I.O. Timer Array at location 7B on the board.
- 3 beeps: (will not occur)
- 4 beeps: ROM failure
- 5 beeps: INTERRUPT failure. Check 6532 at location 7B.

In addition, this message appears during power up **only** if the Audio Board needs to be checked:

AUDIO SELF TEST ERROR

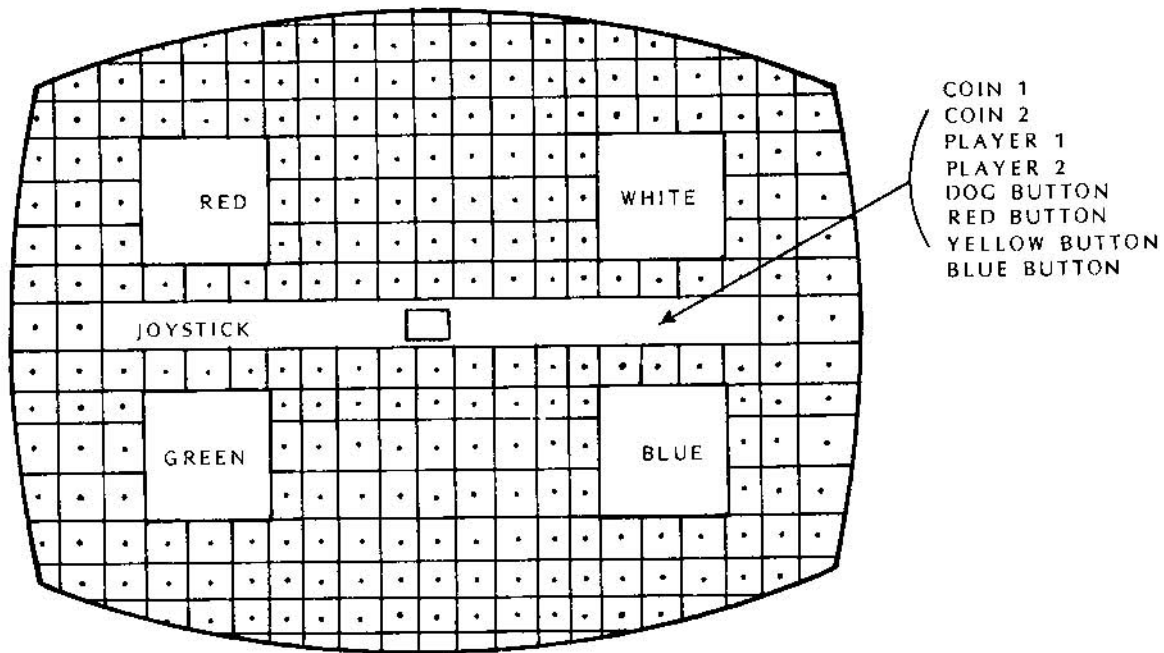
or

AUDIO COMMUNICATION ERROR

3.2 CONTROL AND COLOR TEST MODE

If desired, you may test all player controls and screen colors. To do so, activate either coin switch at power-up. The game then enters the Control/Color Test Mode. This mode lasts for seven minutes to give you time to test and make adjustments. After that, the Attract Mode begins.

In the Control/Color Test Mode, the screen shows this pattern:



The grid pattern tests your screen for any distortion or convergence.

The solid blocks of color (red, white, green, and blue) allow you to adjust the color. When any player control (player 1 start, player 2 start, or red, blue, yellow, or Dog button) or coin switch (coin 1 or coin 2) is activated, its name appears below the white box. This shows that the switch connection is proper. When the joystick is activated, the message joystick appears below the red box. Also, in the middle of the screen, a square indicates the joystick direction.

4.0 NORMAL OPERATION

4.1 ATTRACT MODE

After the MOUSE TRAP™ is powered up and the self- diagnostic test is performed, the Attract Mode starts up, showing the following messages:

MOUSETRAP™

CREDITS 00

ONE COIN FOR ONE CREDIT

ONE CREDIT FOR ONE PLAYER
TWO CREDITS FOR TWO PLAYERS

EXIDY

(P) COPYRIGHT 1981 (C)

The number of coins or any currency per game is operator selectable and the message will reflect that choice.

MOUSETRAP HALL OF FAME

LWH	49720	INCREDIBLE
DJS	47340	FABULOUS
VIC	44130	GREAT
MRK	37830	SUPER
HAI	25650	GOOD

JOIN THE MOUSETRAP HALL OF FAME
BY ENTERING YOUR INITIALS
FOR A HIGH SCORE

At power up, this Hall of Fame contains fictitious high scores. The first player to exceed the lowest score may enter his score and initials. The Hall of Fame will show the high player's score until power is turned off, and then the fictitious scores reappear.

The following game instructions appear, in pictorial form:

MOUSE EATS CHEESE, BONES, TREASURES

DOG EATS CAT, CHEESE, BONES, TREASURES

CAT EATS MOUSE

HAWK EATS MOUSE AND DOG

IN MAKES HAWK STUPID

BONE TURNS MOUSE INTO DOG
WHEN YOU USE THE DOG BUTTON

A few seconds of game play is then shown and the Attract Mode cycle begins again.

4.2 Game Play

Despite many cats on the prowl, the player steers a mouse through a playfield of halls where the mouse chomps cheese for points and finds other red prizes for bonus value.

The mouse searches out dog bones which are accumulated at the top of the screen. Each bone allows the mouse to become a ferocious dog when the "dog button" on the control panel is pressed. Now the player is the aggressor, and cats are open game for additional points of escalating value. After several seconds, the red dog begins blinking blue. This warns that his power is wearing off and he'll soon be a mere mouse again.

When all the cheese morsels on one level are eaten up, the player is given another game field with more challenge.

Randomly, a purple hawk flies over the walls in search of a delicious mouse or dog. The player's only defense is to enter the secret passage marked "in" at the center of the screen. This quickly transports the mouse or dog through an unseen tunnel to one of the four corners. This tactic makes the hawk "dumb", or aimless in his flight. The mouse or dog can then escape the hawk.

The player has the added strategy of doors to open and shut to his advantage. At the touch of a button, the player may open or close three colors of doors. By closing a door, the mouse can block a cat's advance. Or, when the mouse turns into a dog, the skilled player can use the doors to trap cats, earning additional points of escalating value for each one overtaken.

4.3 BONUS TURN FEATURE

An extra turn is given to a player everytime he scores 30,000, 40,000, 50,000, or 60,000 (operator selectable) points. A good player may rack up any number of bonus turns throughout a game, but the screen does not display more than five.

4.4 ENTERING THE MOUSE TRAP™ HALL OF FAME

Anytime a player's score exceeds one of the five current high scores, he is eligible to enter his initials in the Vanity Table. If both players of a two player game are record high scorers, the highest of the two is first invited to enter his initials. To do so, he directs the mouse around an alphabet chart with the joystick. Once on the letter desired, he hits the DOG button. The RUB feature allows a player to erase mistaken letters. Once the correct initials are keyed in, the player should go to END, and hit the DOG button. This will log in his initials, and return to the next mode.

The Hall of Fame Mode runs on a timer. If a player does not enter his or her initials, the Attract Mode eventually takes over.

LOGIC BOARD ASSEMBLY PARTS LIST

PART NUMBER	DESCRIPTION
177-3391-14	Logic PCB Assembly
77-3391-14	Logic PCB
58-0002-00	Spacer
100-0005-00	Ribbon Cable Assembly
22-0001-02	I.C. 74LS00
22-0002-02	I.C. 74LS02
22-0003-02	I.C. 74LS04
22-0004-00	I.C. 7407
22-0005-02	I.C. 74LS08
22-0006-02	I.C. 74LS11
22-0007-02	I.C. 74LS20
22-0008-02	I.C. 74LS21
22-0009-02	I.C. 74LS27
22-0010-02	I.C. 74LS32
22-0011-02	I.C. 74LS74
22-0012-02	I.C. 74LS112
22-0013-02	I.C. 74LS138
22-0214-02	I.C. 74LS139
22-0015-02	I.C. 74LS157
22-0016-02	I.C. 74LS161
22-0017-02	I.C. 74LS166
22-0018-02	I.C. 74LS193
22-0019-02	I.C. 74LS241
22-0020-02	I.C. 74LS245
22-0021-02	I.C. 74LS374
25-0001-00	I.C. MICROPROCESSOR 6502A
23-0001-00	6301 PROM
23-0002-00	6331 PROM
23-0003-00	6331 PROM
20-0001-00	IN4002 DIODE
02-4712-00	RESISTOR, 470 OHM 1/4W 5%
02-1222-00	RESISTOR, 1.2K 1/4W 5%
02-2222-00	RESISTOR, 2.2K 1/4W 5%
02-3322-00	RESISTOR, 3.3K 1/4W 5%
09-2212-00	RESISTOR, 220 OHM 1/8W 5% 10 PIN SIP
09-2222-00	RESISTOR, 2.2K 1/8W 5% 10 PIN SIP
09-4722-00	RESISTOR, 4.7K 1/8W 5% 10 PIN SIP
09-6822-00	RESISTOR, 6.8K 1/8W 5% 10 PIN SIP
10-1034-1	CAPACITOR, .01 UF CERAMIC DISC
10-1044-1	CAPACITOR, .1 UF CERAMIC DISC
11-6853-0	CAPACITOR, 6.8 UF 25V DIPTANT
13-4775-0	CAPACITOR, 470UF 10V ELECTROLYTIC
47-0001-00	DIPSHUNT JUMPER PAKS 16 PIN
49-5002-00	DIP SWITCH 8 POSITION
29-0001-00	CRYSTAL 11.289MHZ (SERIES)
44-1601-00	DIP SOCKETS 16 PIN LOW PROFILE
44-2401-00	DIP SOCKETS 24 PIN LOW PROFILE
44-4001-00	DIPSOCKETS 40 PIN LOW PROFILE
44-1401-00	DIP SOCKETS 14 PIN LOW PROFILE
44-1801-00	DIP SOCKETS 18 PIN LOW PROFILE
11-1053-00	CAPACITOR 1UF 25V DIPTANT
02-2212-00	RESISTOR, 220 OHM 1/4W 5%

LOGIC BOARD ASSEMBLY PARTS LIST (continued)

PART NUMBER	DESCRIPTION
02-1802-00	RESISTOR, 18 OHM 1/4W 5%
10-3314-4	CAPACITOR, 330PF CERAMIC DISC
02-4732-00	RESISTOR, 47K
11-1063-00	10UF 16V DIPTANT
23-0004-00	2732, EPROM
23-0005-00	2716, EPROM
23-0006-00	2114 STATIC RAM
02-1822-00	RESISTOR, 1.8K 1/4W 5%
02-2272-00	RESISTOR, 2.7K 1/4W 5%
02-1052-00	RESISTOR, 1M 1/4W 5%
27-0003-00	I.C., OSC NE555
20-0006-00	DIODE, 4454
23-0014-00	2732, EPROM
23-0015-00	2732, EPROM
23-0016-00	2732, EPROM
23-0017-00	2732, EPROM
23-0018-00	2732, EPROM

AUDIO/COLOR BOARD ASSEMBLY

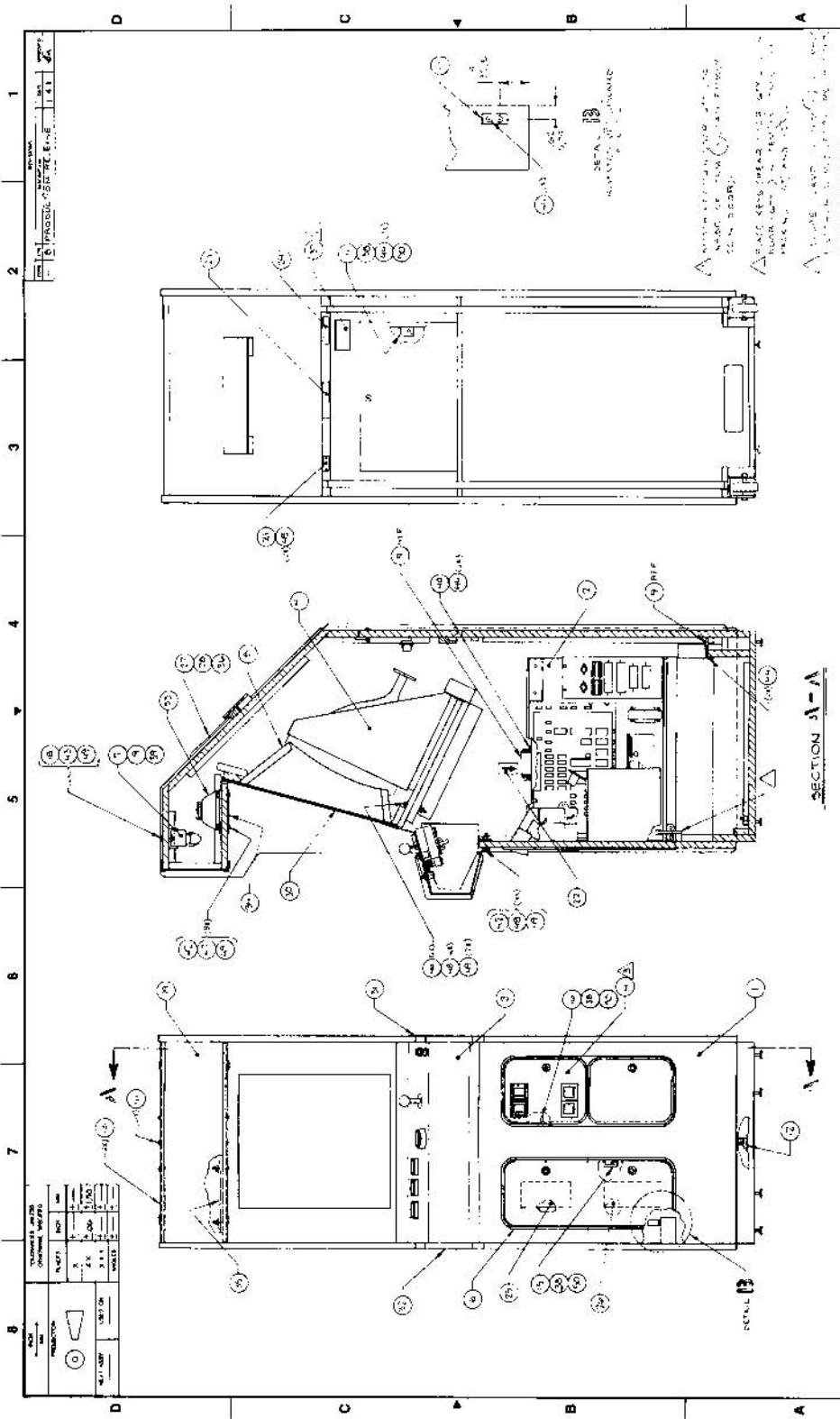
PART NUMBER	DESCRIPTION
177-3392-14	AUDIO/COLOR PCB ASSEMBLY
77-3392-14	AUDIO/COLOR PCB
22-0013-00	I.C. 74LS138
26-0001-00	I.C. 6520-A
22-0023-00	I.C. 4069
22-0003-02	I.C. 74LS04
22-0001-02	I.C. 74LS00
22-0021-02	I.C. 74LS374
22-0024-00	I.C. 4053
22-0025-00	I.C. 4175
22-0011-02	I.C. 74LS74
22-0026-00	I.C. LM324
22-0027-02	I.C. 74LS86
22-0028-00	I.C. 4051
22-0014-02	I.C. 74LS139
22-0029-02	I.C. 74LS148
22-0030-02	I.C. 74LS151
22-0031-02	I.C. 74LS174
10-1044-1	CAPACITOR, .1 UF CERAMIC
10-2204-1	CAPACITOR, 22PF 16V CERAMIC
10-1034-1	CAPACITOR, .01UF 16V CERAMIC
13-3365-1	CAPACITOR, 33UF 25V ELECTROLYTIC
13-1075-00	CAPACITOR, 100UF 16V ELECTROLYTIC
13-4755-00	CAPACITOR, 4.7UF 16V ELECTROLYTIC
02-3332-00	RESISTOR, 33K 1/4W 5%
02-3322-00	RESISTOR, 3.3K 1/4W 5%
02-1062-00	RESISTOR, 10M 1/4W 5%
02-3012-00	RESISTOR, 300 OHM 1/4W 5%
02-1802-00	RESISTOR, 18 1/4W 5%
02-2232-00	RESISTOR, 22K 1/4W 5%
02-2722-00	RESISTOR, 2.7K 1/4W 5%

AUDIO/COLOR BOARD ASSEMBLY (continued)

PART NUMBER	DESCRIPTION
02-1322-00	RESISTOR, 1.3K 1/4W 5%
02-6812-00	RESISTOR, 680 OHM 1/4W 5%
02-3312-00	RESISTOR, 330 OHM 1/4W 5%
02-1612-00	RESISTOR, 160 OHM 1/4W 5%
02-8202-00	RESISTOR, 82 OHM 1/4W 5%
02-3902-00	RESISTOR, 39 OHM 1/4W 5%
02-1032-00	RESISTOR, 10K 1/4W 5%
07-1034-00	10K POT
29-0002-00	3.579545 MHZ CRYSTAL
47-0001-00	16 PIN DIP SHUNT
44-1401-00	14 PIN DIP SOCKET
44-2401-00	24 PIN DIP SOCKET
44-2801-00	28 PIN DIP SOCKET
44-4001-00	40 PIN DIP SOCKET
44-1601-00	16 PIN DIP SOCKET
02-4712-00	RESISTOR, 470 OHM 1/4
02-2422-00	RESISTOR, 2.4K 1/4W
02-2032-00	RESISTOR, 20K 1/4W
02-1832-00	RESISTOR, 18K 1/4W
02-3922-00	RESISTOR, 3.9K 1/4W
02-2452-00	RESISTOR, 2.4M 1/4/W
22-0039-00	I.C. 74LS42
23-0007-00	2732 EPROM
23-0008-00	2732 EPROM
23-0009-00	2732 EPROM
10-2034-01	CAPACITOR .02 uf CER.
23-0010-00	2732 EPROM
27-0003-00	NE555 OSC.
22-0033-00	TL081 OP/AMP
27-0004-00	MC3417L DELTA MOD
22-0034-00	4006 CMOS SHIFT REG.
40-0003-00	6 PIN MALE CONNECTOR .156 CENTER
22-0037-00	I.C. 74LS125
22-0015-00	I.C. 74LS157
22-0010-00	I.C. 74LS32
22-0038-00	I.C. 74LS367
25-0003-00	Z80 CPU
22-0035-00	4070 CMOS EXC. OR GATE
22-0036-00	LM741EN OP/AMP
02-5643-01	RESISTOR, .560K OHM 1/4W
84-0014-00	HEATSINK, THM6045
14-2044-00	CAPACITOR, MYLAR .2UF 16V
10-1024-00	CAPACITOR, CERAMIC .001UF 16V
09-0001-00	RESISTOR, 1.8K SIP PAC 8 PIN
09-0002-00	RESISTOR, 4.7K SIP PAC 8 PIN
21-0001-00	NPN TRANSISTOR TIP 120
21-0002-00	PNP TRANSISTOR, TIP 125
10-5024-01	CAPACITOR, MYLAR .0022 UF
14-2224-00	CAPACITOR, MYLAR .0022 uf
23-0011-00	2716, EPROM
23-0012-00	2716, EPROM
23-0013-00	2716, EPROM

AUDIO/COLOR BOARD ASSEMBLY (continued)

PART NUMBER	DESCRIPTION
13-3355-01	CAPACITOR .33 uf 16V ELECTROLYTIC
40-0004-00	CONNECTOR, PCB MALE
25-0002-00	I.C. 6532
25-0004-00	I.C. 6502
27-0001-00	I.C. 6840
22-0022-02	I.C. 74LS154
27-0002-00	I.C. 8253

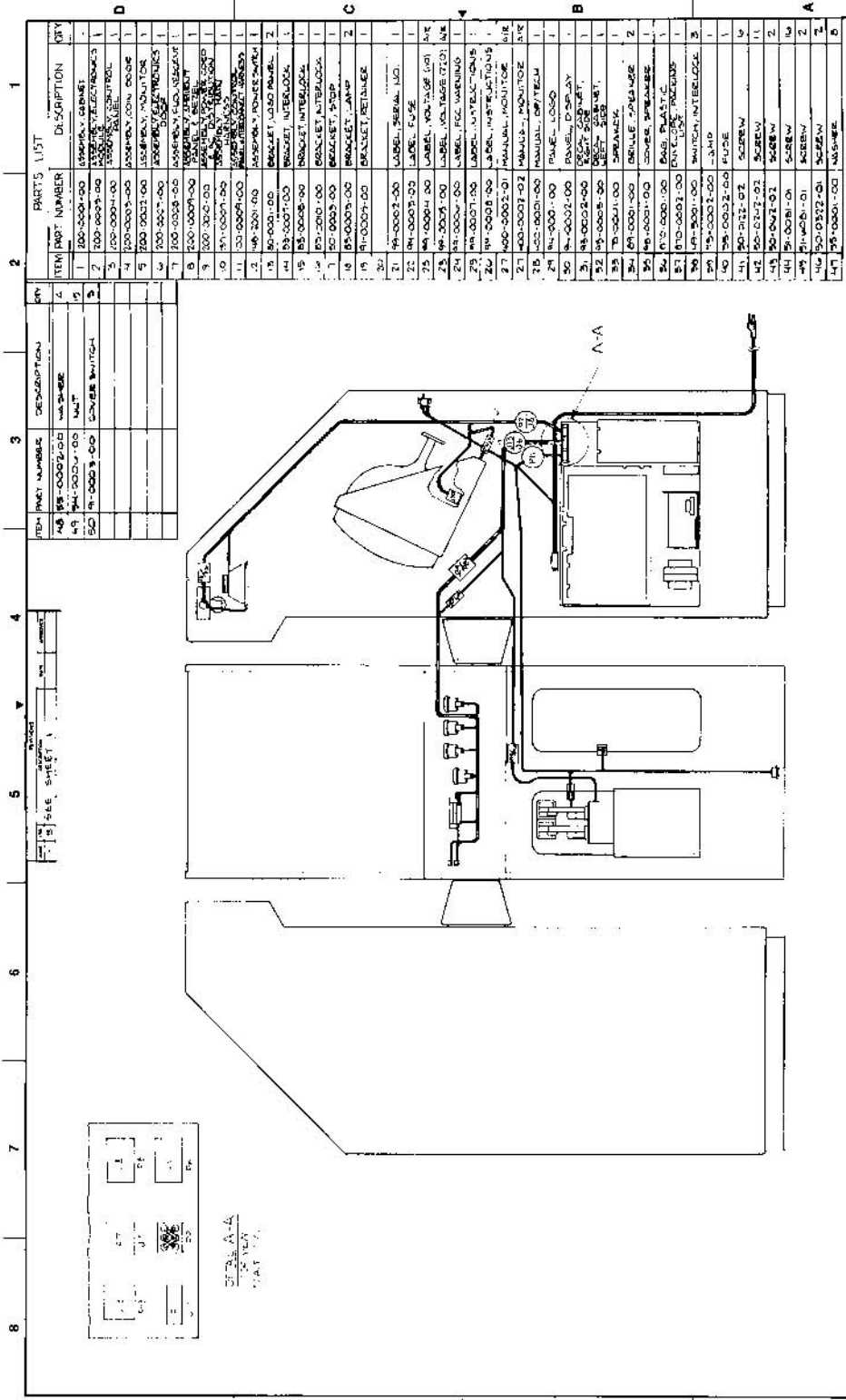


SECTION A-A

PROJ. NO.	1000
REV.	1
DATE	10/1/50
BY	J. H. B.
CHECKED BY	J. H. B.
APPROVED BY	J. H. B.

THICKNESS	0.005
DRILLING	0.002
FINISH	0.001
PLATE	0.001
WELDING	0.001
OTHER	0.001

PROJECT NO.	1000
REV.	1
DATE	10/1/50
BY	J. H. B.
CHECKED BY	J. H. B.
APPROVED BY	J. H. B.
DESIGNER	J. H. B.
ENGINEER	J. H. B.
DATE	10/1/50
SCALE	AS SHOWN
PROJECT	SECTION A-A
DESCRIPTION	ASSEMBLY, FINAL



ITEM	PART NUMBER	DESCRIPTION	QTY
1	100-0001-00	ASSEMBLY, CHASSIS	1
2	100-0002-00	ASSEMBLY, CONTROL	1
3	100-0003-00	ASSEMBLY, DISPLAY	1
4	100-0004-00	ASSEMBLY, POWER SUPPLY	1
5	100-0005-00	ASSEMBLY, FUSE HOLDER	1
6	100-0006-00	ASSEMBLY, SWITCH	1
7	100-0007-00	ASSEMBLY, BATTERY	1
8	100-0008-00	ASSEMBLY, LABEL	1
9	100-0009-00	ASSEMBLY, LABEL	1
10	100-0010-00	ASSEMBLY, LABEL	1
11	100-0011-00	ASSEMBLY, LABEL	1
12	100-0012-00	ASSEMBLY, LABEL	1
13	100-0013-00	ASSEMBLY, LABEL	1
14	100-0014-00	ASSEMBLY, LABEL	1
15	100-0015-00	ASSEMBLY, LABEL	1
16	100-0016-00	ASSEMBLY, LABEL	1
17	100-0017-00	ASSEMBLY, LABEL	1
18	100-0018-00	ASSEMBLY, LABEL	1
19	100-0019-00	ASSEMBLY, LABEL	1
20	100-0020-00	ASSEMBLY, LABEL	1
21	100-0021-00	ASSEMBLY, LABEL	1
22	100-0022-00	ASSEMBLY, LABEL	1
23	100-0023-00	ASSEMBLY, LABEL	1
24	100-0024-00	ASSEMBLY, LABEL	1
25	100-0025-00	ASSEMBLY, LABEL	1
26	100-0026-00	ASSEMBLY, LABEL	1
27	100-0027-00	ASSEMBLY, LABEL	1
28	100-0028-00	ASSEMBLY, LABEL	1
29	100-0029-00	ASSEMBLY, LABEL	1
30	100-0030-00	ASSEMBLY, LABEL	1
31	100-0031-00	ASSEMBLY, LABEL	1
32	100-0032-00	ASSEMBLY, LABEL	1
33	100-0033-00	ASSEMBLY, LABEL	1
34	100-0034-00	ASSEMBLY, LABEL	1
35	100-0035-00	ASSEMBLY, LABEL	1
36	100-0036-00	ASSEMBLY, LABEL	1
37	100-0037-00	ASSEMBLY, LABEL	1
38	100-0038-00	ASSEMBLY, LABEL	1
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40	100-0040-00	ASSEMBLY, LABEL	1
41	100-0041-00	ASSEMBLY, LABEL	1
42	100-0042-00	ASSEMBLY, LABEL	1
43	100-0043-00	ASSEMBLY, LABEL	1
44	100-0044-00	ASSEMBLY, LABEL	1
45	100-0045-00	ASSEMBLY, LABEL	1
46	100-0046-00	ASSEMBLY, LABEL	1
47	100-0047-00	ASSEMBLY, LABEL	1

SEE PARTS LIST FOR DESCRIPTIONS

SEE PARTS LIST FOR QUANTITIES

SEE PARTS LIST FOR MATERIAL SPECIFICATIONS

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SEE PARTS LIST FOR ACCESSORIES

SEE PARTS LIST FOR SUPPLIES

SEE PARTS LIST FOR TOOLS

SEE PARTS LIST FOR EQUIPMENT

ASSEMBLY, FINAL

ASSEMBLY, INITIAL

ASSEMBLY, PRELIMINARY

ASSEMBLY, CONCEPT

ASSEMBLY, PROTOTYPE

ASSEMBLY, TEST

ASSEMBLY, DEMO

ASSEMBLY, TRAINING

ASSEMBLY, REPAIR

ASSEMBLY, MAINTENANCE

ASSEMBLY, OVERHAUL

ASSEMBLY, RESTORATION

ASSEMBLY, REPRODUCTION

ASSEMBLY, REPAIR KIT

ASSEMBLY, MAINTENANCE KIT

ASSEMBLY, OVERHAUL KIT

ASSEMBLY, RESTORATION KIT

ASSEMBLY, REPRODUCTION KIT

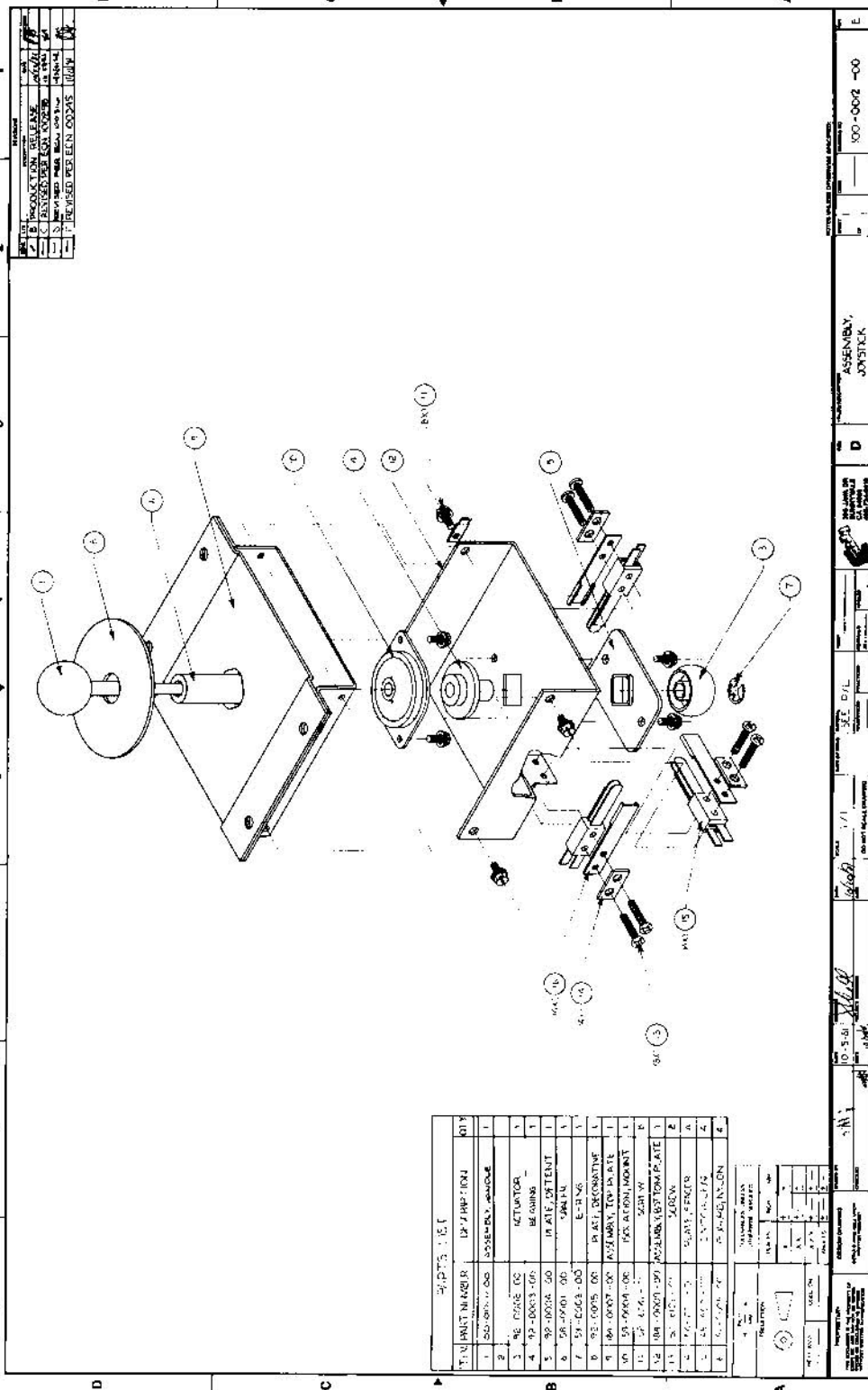
ASSEMBLY, REPAIR KIT

ASSEMBLY, MAINTENANCE KIT

ASSEMBLY, OVERHAUL KIT

ASSEMBLY, RESTORATION KIT

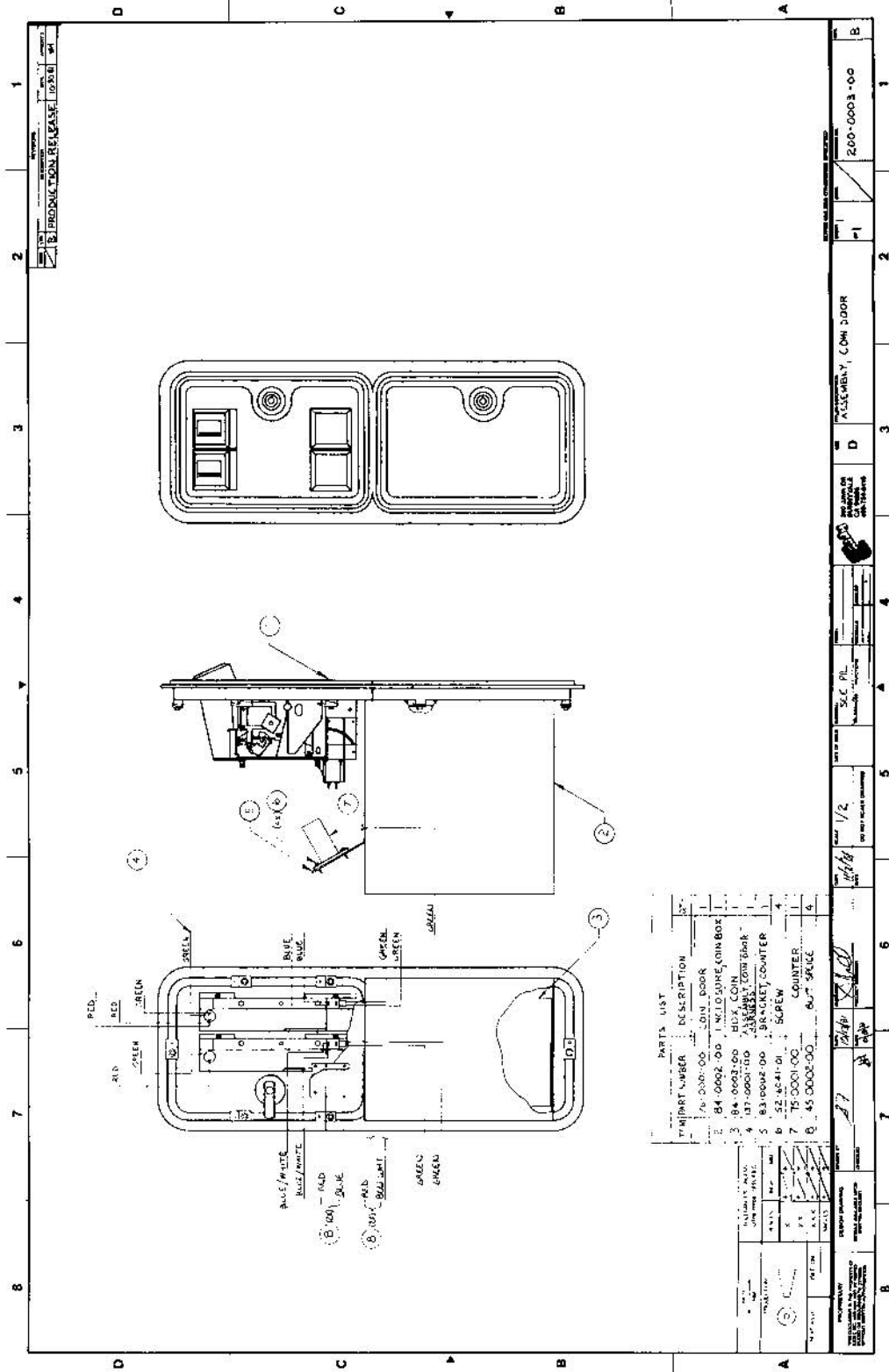
ASSEMBLY, REPRODUCTION KIT



1	PRODUCTION RELEASE	AS/01
2	REVISIONS FOR CHANGE	AS/01
3	REVISIONS FOR CHANGE	AS/01
4	REVISIONS FOR CHANGE	AS/01
5	REVISIONS FOR CHANGE	AS/01
6	REVISIONS FOR CHANGE	AS/01
7	REVISIONS FOR CHANGE	AS/01

ITEM NUMBER	DESCRIPTION	QTY
1	ACTUATOR	1
2	BEARING	1
3	PLATE/STENT	1
4	WASHER	1
5	E-RINGS	1
6	PLATE/DECOMPRATE	1
7	PLATE/DECOMPRATE	1
8	PLATE/DECOMPRATE	1
9	PLATE/DECOMPRATE	1
10	PLATE/DECOMPRATE	1
11	PLATE/DECOMPRATE	1
12	PLATE/DECOMPRATE	1
13	PLATE/DECOMPRATE	1
14	PLATE/DECOMPRATE	1
15	PLATE/DECOMPRATE	1
16	PLATE/DECOMPRATE	1

DATE	1/1
BY	AS/01
CHKD BY	AS/01
APPROVED	AS/01
REVISIONS	AS/01
DESCRIPTION	AS/01
QUANTITY	AS/01
UNIT	AS/01
ASSEMBLY	AS/01
JOINTICK	AS/01
100-0002-00	AS/01

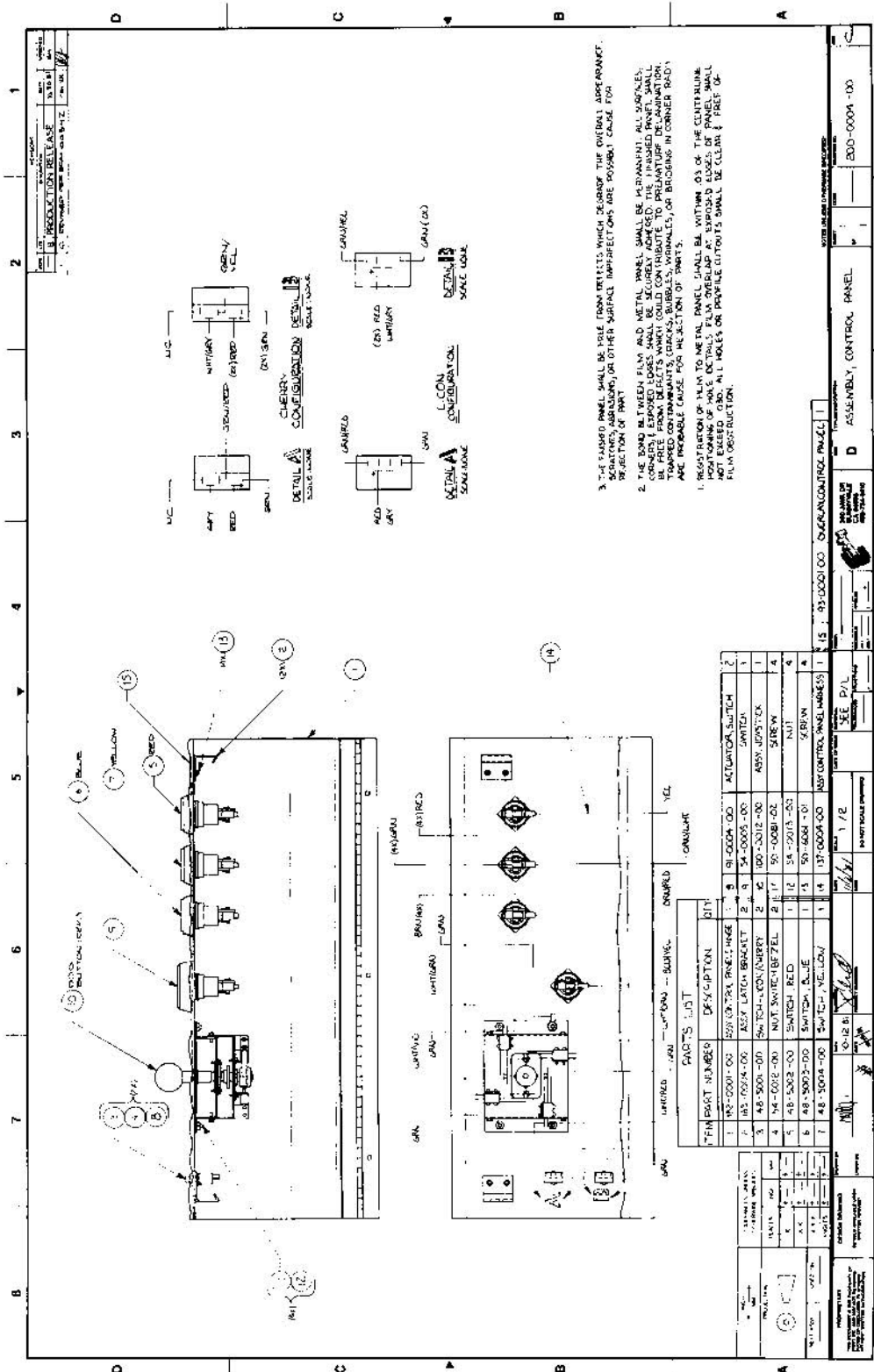


PRODUCTION RELEASE 1978

PARTS LIST

PART NUMBER	DESCRIPTION	QTY
76-0001-00	COIN DOOR	1
84-0002-00	INLET COIN BOX	1
84-0003-00	BOX, COIN	1
137-0001-00	ASSEMBLY COIN DOOR	1
83-0002-00	BRACKET, COIN	1
52-8041-01	SCREW	4
75-0001-00	COUNTER	1
85-0003-00	OUT SAUCE	4

DRAWING NUMBER: 77
 TITLE: COIN DOOR ASSEMBLY
 DATE: 1/2
 SCALE: 1/2
 DESIGNED BY: [Signature]
 CHECKED BY: [Signature]
 APPROVED BY: [Signature]
 PART NUMBER: 76-0001-00
 DESCRIPTION: COIN DOOR ASSEMBLY, COIN DOOR
 QUANTITY: 1
 UNIT: 200-0003-00



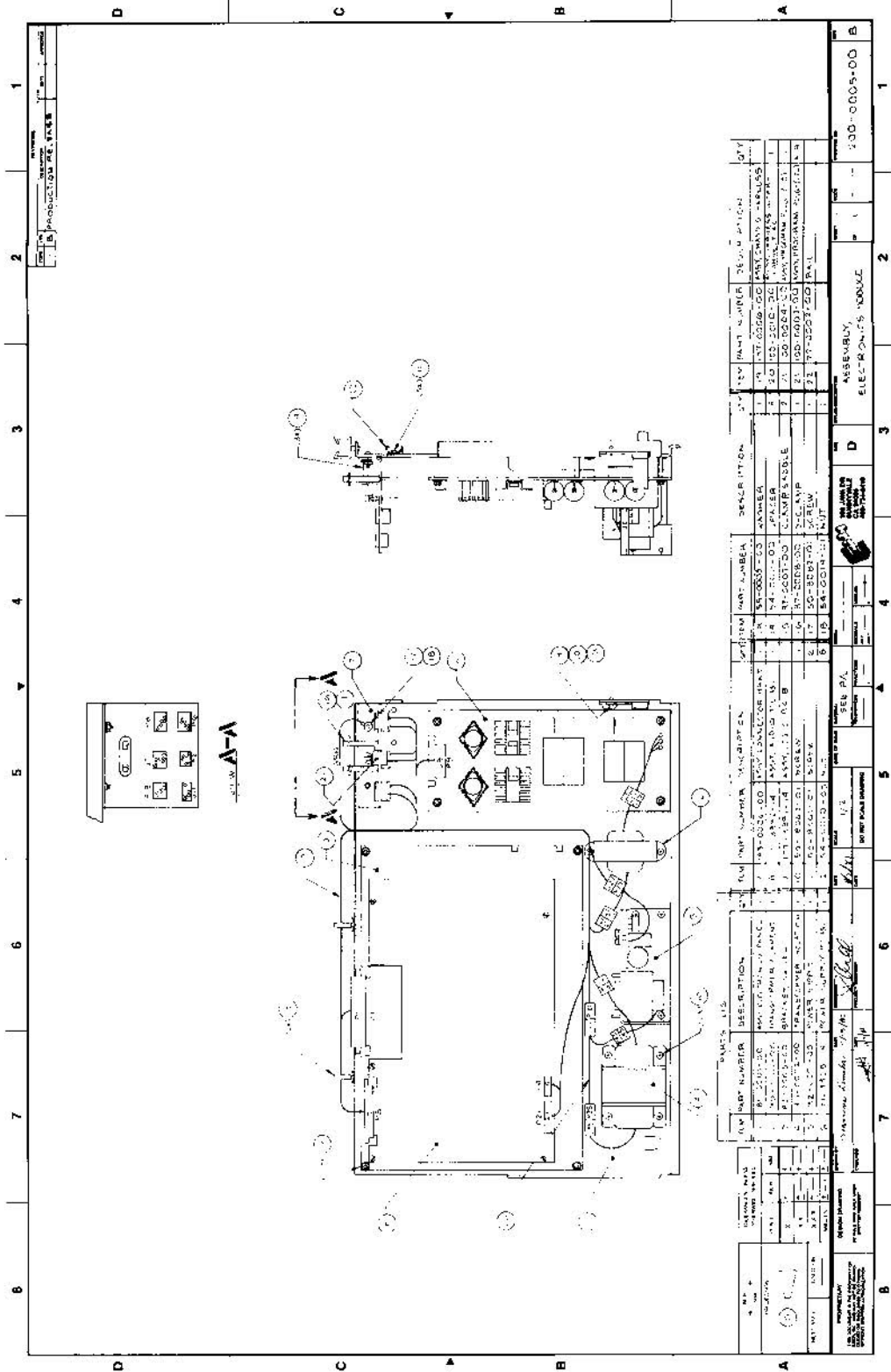
1. THE FINISHED PANEL SHALL BE VISIBLE FROM PERISCOPE WHICH OBSERVE THE OVERALL APPEARANCE OF THE CONTROL PANEL. UNLESS OTHERWISE SPECIFIED, ALL DIMENSIONS ARE IN INCHES. DIMENSIONS IN PARENTHESES ARE FOR REFERENCE ONLY.

2. THE GROUND BETWEEN FILM AND METAL PANEL SHALL BE PERMANENT. ALL SURFACES OF THE FINISHED PANEL SHALL BE SECURELY ADHERED TO PREVENTURE DETACHMENT. ALL FREE FROM DEFECTS WHICH COULD CONTRIBUTE TO PREVENTURE DETACHMENT. ALL DEFECTS SHALL BE REPAIRABLE. ALL DEFECTS SHALL BE REPAIRABLE. ALL DEFECTS SHALL BE REPAIRABLE.

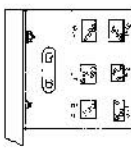
3. THE FINISHED PANEL SHALL BE VISIBLE FROM PERISCOPE WHICH OBSERVE THE OVERALL APPEARANCE OF THE CONTROL PANEL. UNLESS OTHERWISE SPECIFIED, ALL DIMENSIONS ARE IN INCHES. DIMENSIONS IN PARENTHESES ARE FOR REFERENCE ONLY.

ITEM	PART NUMBER	DESCRIPTION	QTY
1	91-0004-00	ACTUATOR SWITCH	2
2	54-0005-00	ASSY. LATCH BRACKET	2
3	100-2012-00	ASSY. NOTCH	1
4	50-0001-02	SCREW	4
5	54-2013-00	NOTCH	4
6	50-6004-01	SCREW	4
7	13-0004-00	ASSY. CONTROL PANEL ADDRESS	1

DATE: 10/12/54
 DRAWN BY: [Signature]
 CHECKED BY: [Signature]
 APPROVED BY: [Signature]
 TITLE: ASSEMBLY CONTROL PANEL
 PROJECT: 200-0004-100
 SHEET: 1 OF 1



1
 2
 3
 4
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 6
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 8



A-A

ITEM NO.	QTY	DESCRIPTION	UNIT	REVISION	DATE	BY	CHKD
1	1	ASSEMBLY	EA	1	10-10-68	J. J. [Signature]	[Signature]
2	1	WASHER	EA	1	10-10-68	J. J. [Signature]	[Signature]
3	1	SCREW	EA	1	10-10-68	J. J. [Signature]	[Signature]
4	1	WASHER	EA	1	10-10-68	J. J. [Signature]	[Signature]
5	1	SCREW	EA	1	10-10-68	J. J. [Signature]	[Signature]
6	1	WASHER	EA	1	10-10-68	J. J. [Signature]	[Signature]
7	1	SCREW	EA	1	10-10-68	J. J. [Signature]	[Signature]
8	1	WASHER	EA	1	10-10-68	J. J. [Signature]	[Signature]

1
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A
 B
 C
 D

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 8

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 B
 C
 D

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 C
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 D

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 8

A
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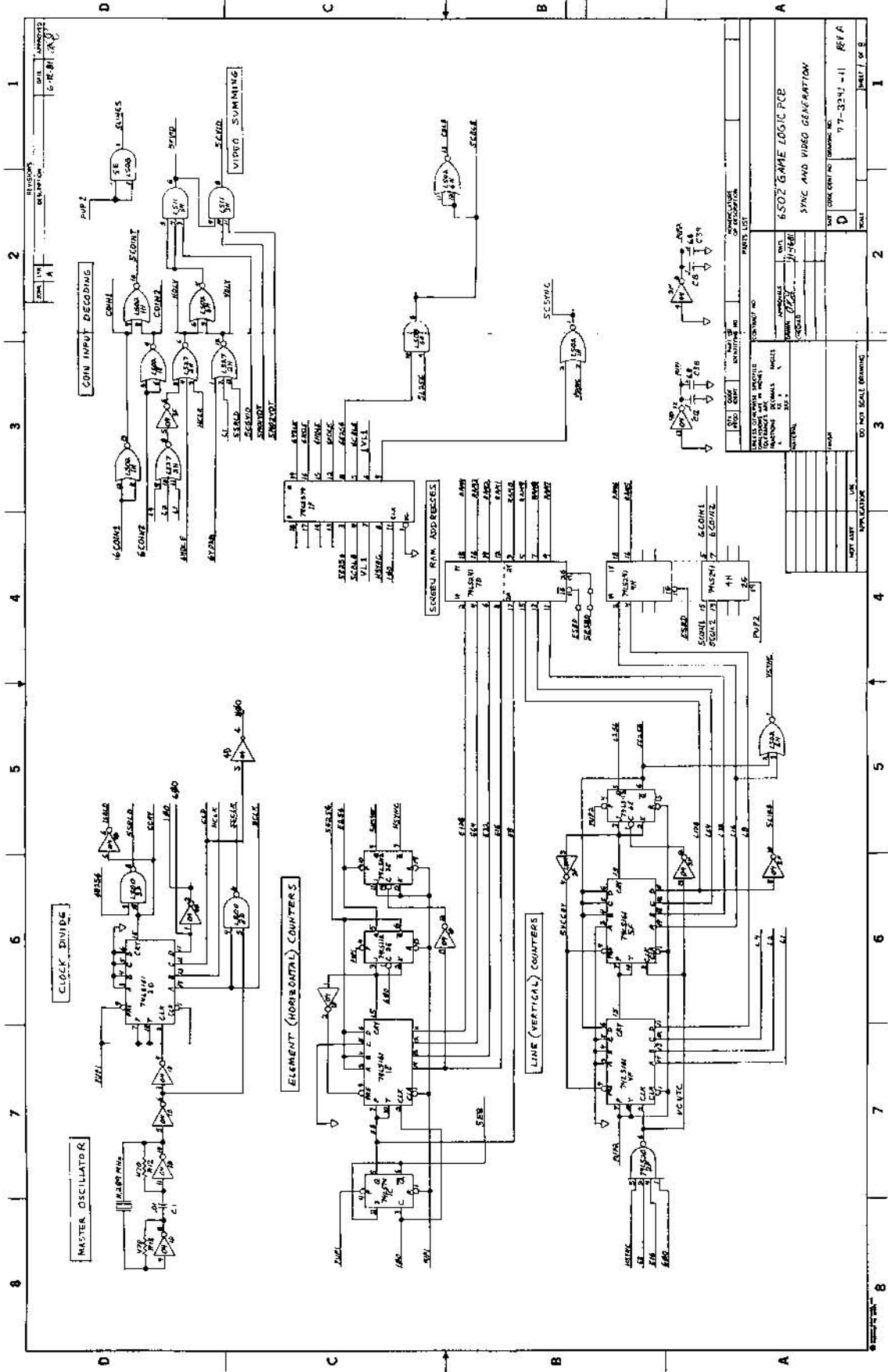
A
 B
 C
 D

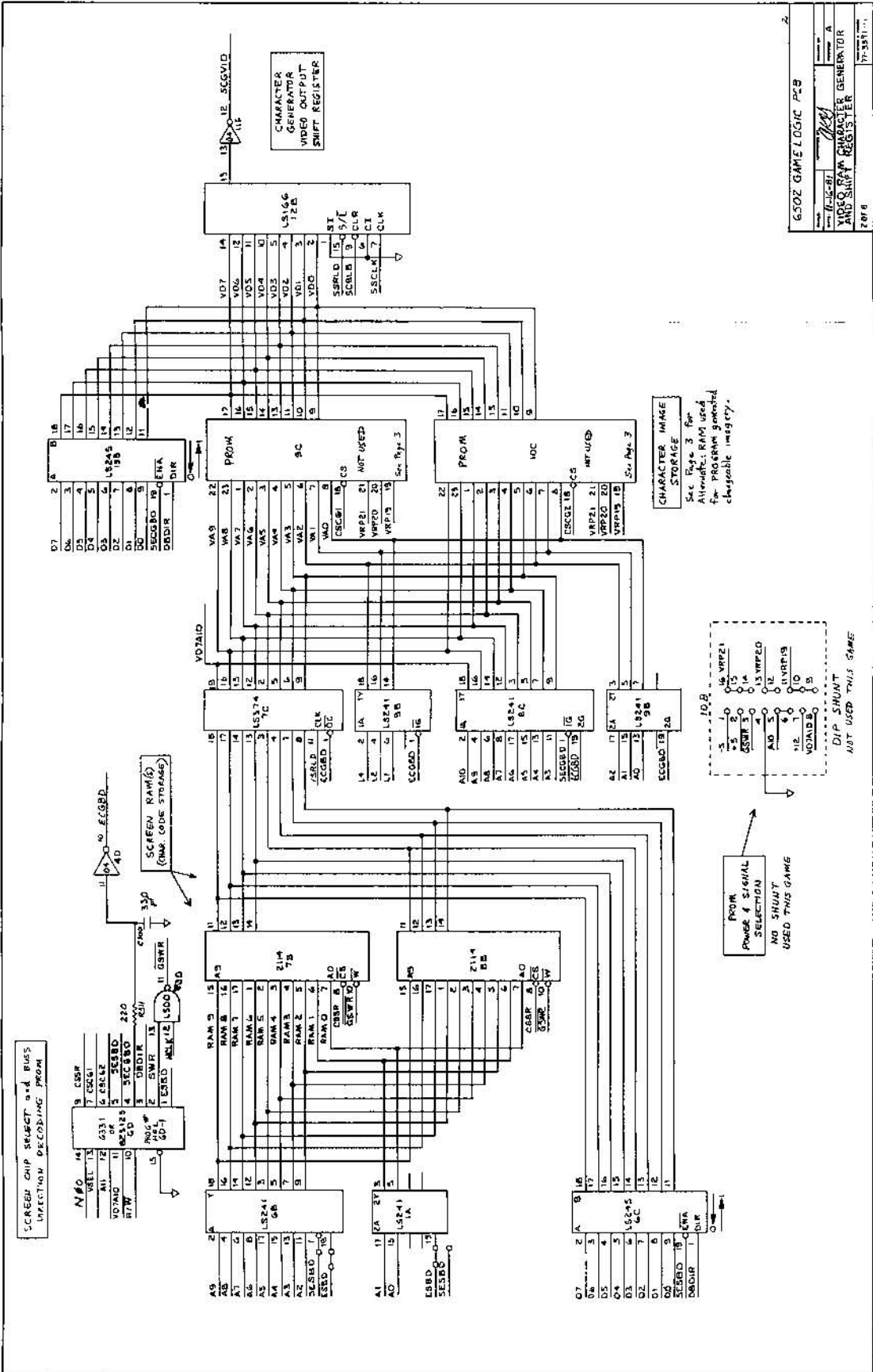
1
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 3
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 8

A
 B
 C
 D

1
 2
 3
 4
 5
 6
 7
 8

A
 B
 C
 D





6502 GAME LOGIC P29
 VIDEO RAM CHARACTER GENERATOR AND SHIFT REGISTER
 7078

CHARACTER IMAGE STORAGE
 See Page 3 for Alternative RAM used for program generated changeable imagery.

IOB
 VERZ1 1
 VERZ2 2
 VERZ3 3
 VERZ4 4
 VERZ5 5
 VERZ6 6
 VERZ7 7
 VERZ8 8
 VERZ9 9
 VERZ10 10
 VERZ11 11
 VERZ12 12
 VERZ13 13
 VERZ14 14
 VERZ15 15
 VERZ16 16
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 VERZ31 31
 VERZ32 32
 VERZ33 33
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 VERZ36 36
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 VERZ64 64
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 VERZ66 66
 VERZ67 67
 VERZ68 68
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 VERZ82 82
 VERZ83 83
 VERZ84 84
 VERZ85 85
 VERZ86 86
 VERZ87 87
 VERZ88 88
 VERZ89 89
 VERZ90 90
 VERZ91 91
 VERZ92 92
 VERZ93 93
 VERZ94 94
 VERZ95 95
 VERZ96 96
 VERZ97 97
 VERZ98 98
 VERZ99 99
 VERZ100 100

PROM 4 SIGNAL SELECTION
 NO SHUNT USED THIS GAME

DIP SHUNT NOT USED THIS GAME

CHARACTER GENERATOR
IMAGE STORAGE RAMS

For use with PROGRAM
generated changeable image.
ALL ZIF'S \leq 300 ns

VAS	15	V07
VAB	16	V06
VAT	17	V05
VAG	1	V04
VAS	2	RAM
VAS	3	214
VAS	4	11C
VAS	5	
VAT	6	
VAD	7	
CSG01	8	
CSWR	10	

VAS	15	V07
VAB	16	V06
VAT	17	V05
VAG	1	V04
VAS	2	RAM
VAS	3	214
VAS	4	11C
VAT	6	
VAD	7	
CSG01	8	
CSWR	10	

VAS	15	V03
VAB	16	V02
VAT	17	V01
VAG	1	V00
VAS	2	RAM
VAS	3	214
VAS	4	11C
VAT	6	
VAD	7	
CSG01	8	
CSWR	10	

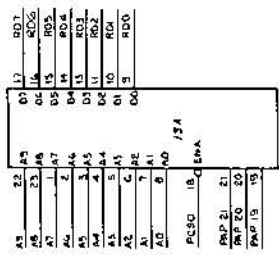
VAS	15	V03
VAB	16	V02
VAT	17	V01
VAG	1	V00
VAS	2	RAM
VAS	3	214
VAS	4	11C
VAT	6	
VAD	7	
CSG01	8	
CSWR	10	

6502 GAME LOGIC PCB

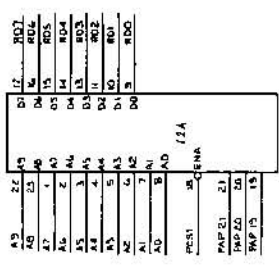
DATE	11-12-81
DESIGNED BY	AKC
CHECKED BY	
APPROVED BY	
IMAGE STORAGE RAM	
3 OF 8	

PROGRAM MEMORY

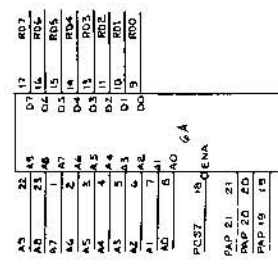
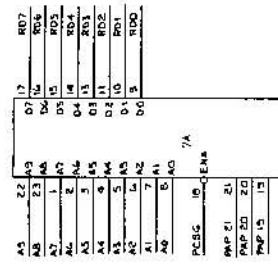
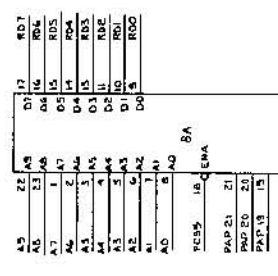
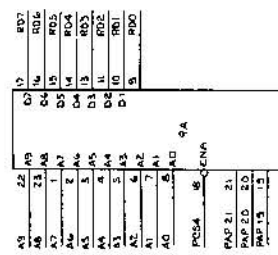
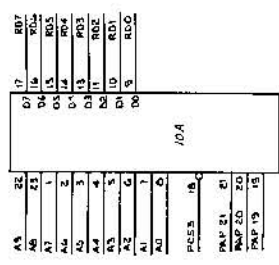
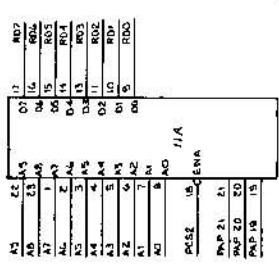
See TECHNICAL MANUAL FOR PROGRAM # 1.



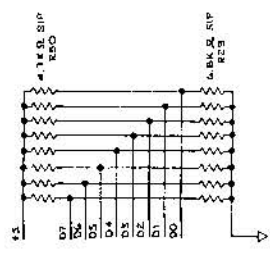
PROM ADDRESS SELECTION
 Configuration shown is for 45V ONLY 2732 EPROMS.
 See TECHNICAL MANUAL for configuration for other memory devices.



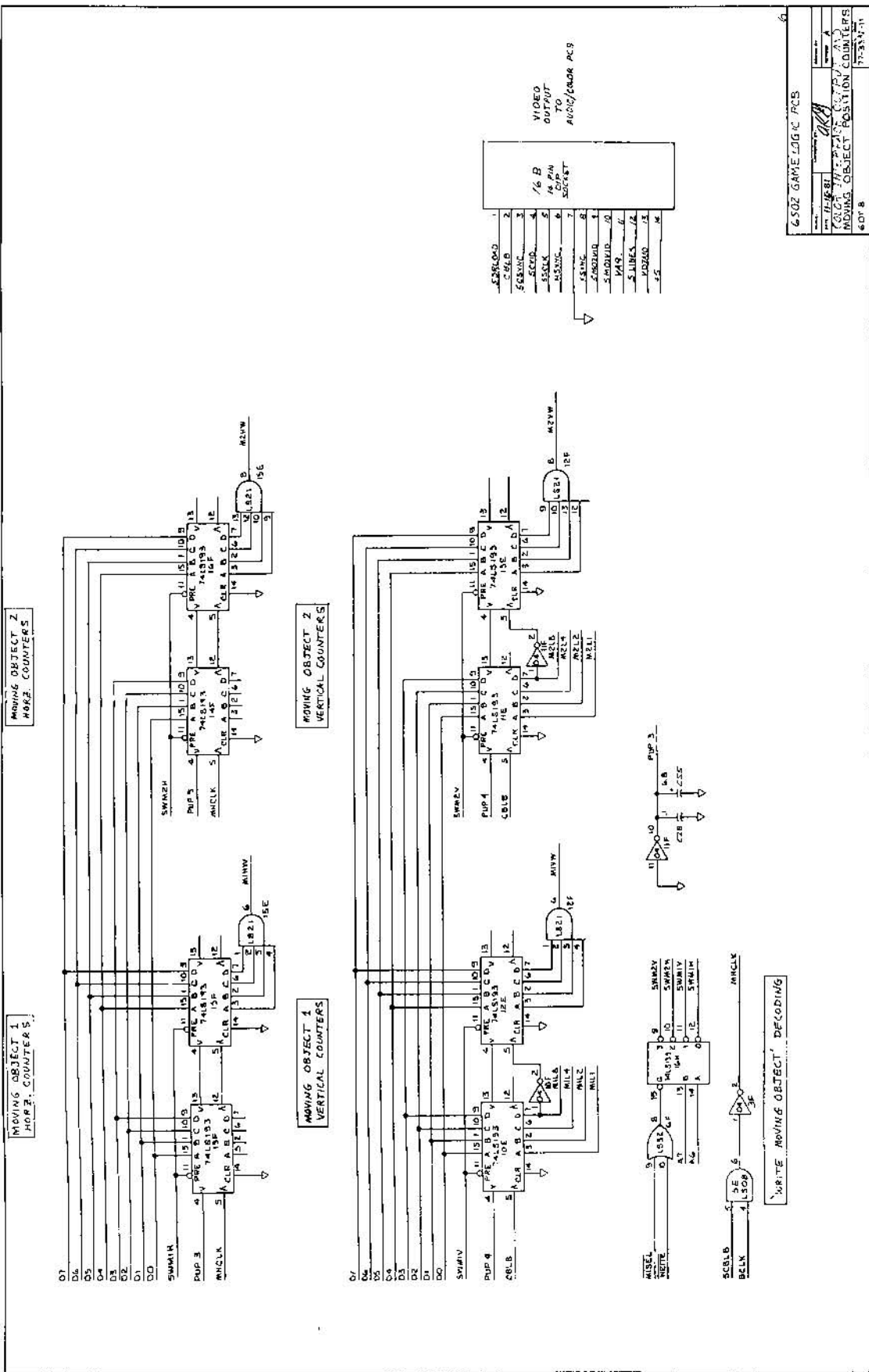
MEMORY DEVICE SELECTION
 Configuration shown is for 45V ONLY 2732 EPROMS.
 See TECHNICAL MANUAL for configuration for other memory devices.



DATA BUSES TERMINATION
 Config. Suppression



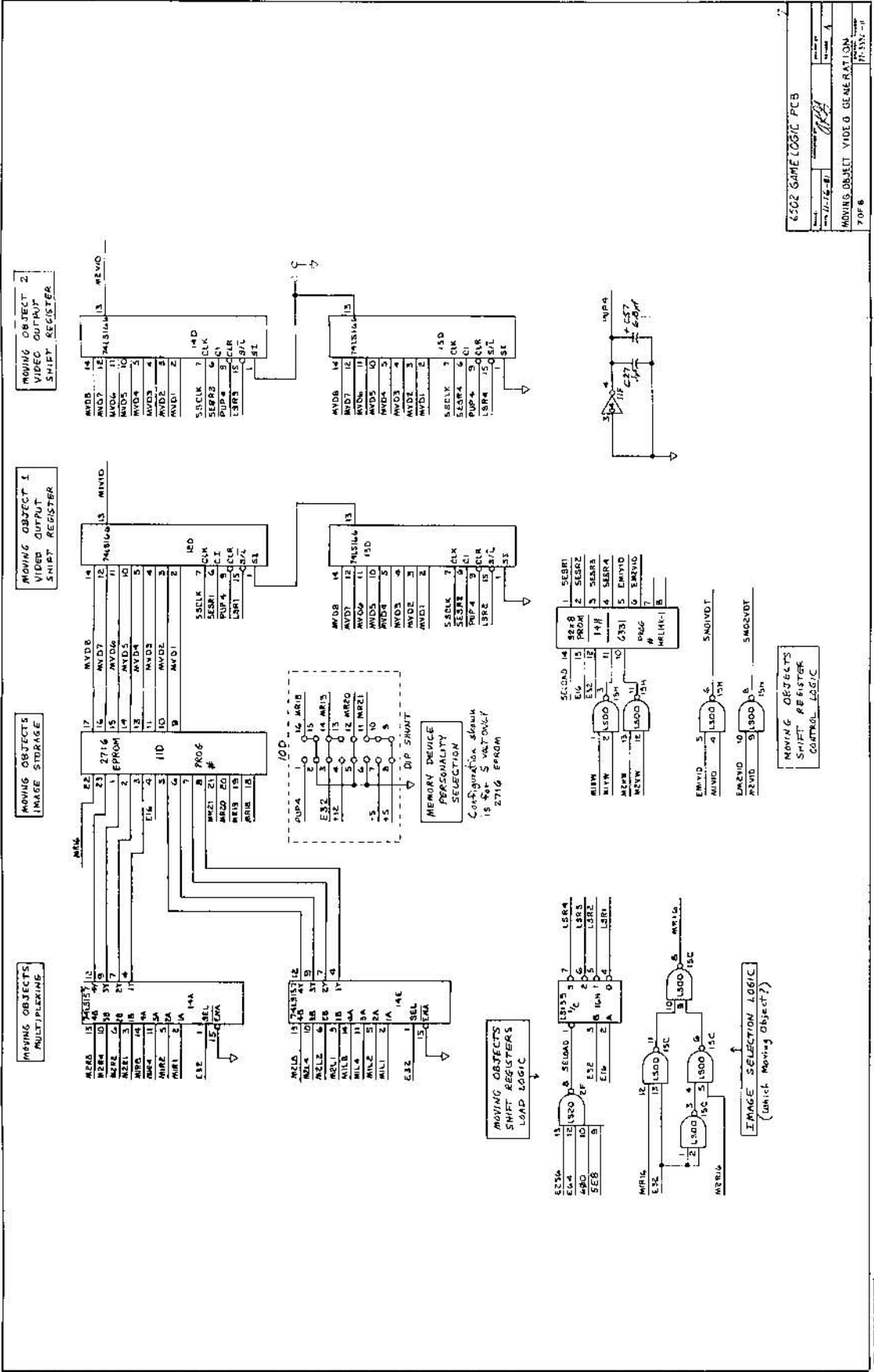
EXIDY INC.
 4502 GARDNER RD
 SHERMAN, CA 94133
 415-438-8088

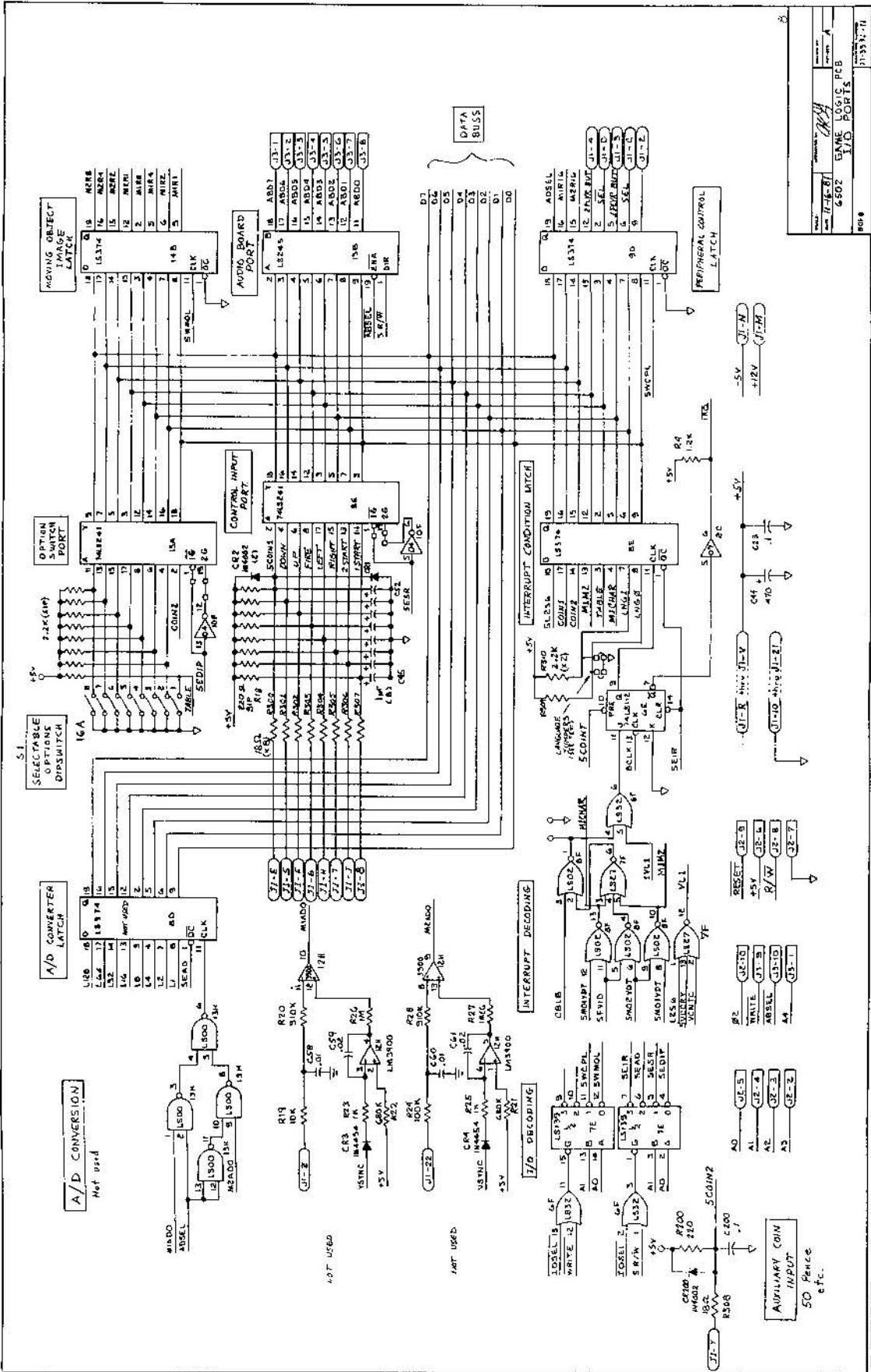


6502 GAME LOGIC PCB
 REV. 7/88
 MOVING OBJECT POSITION COUNTERS
 6 OF 8

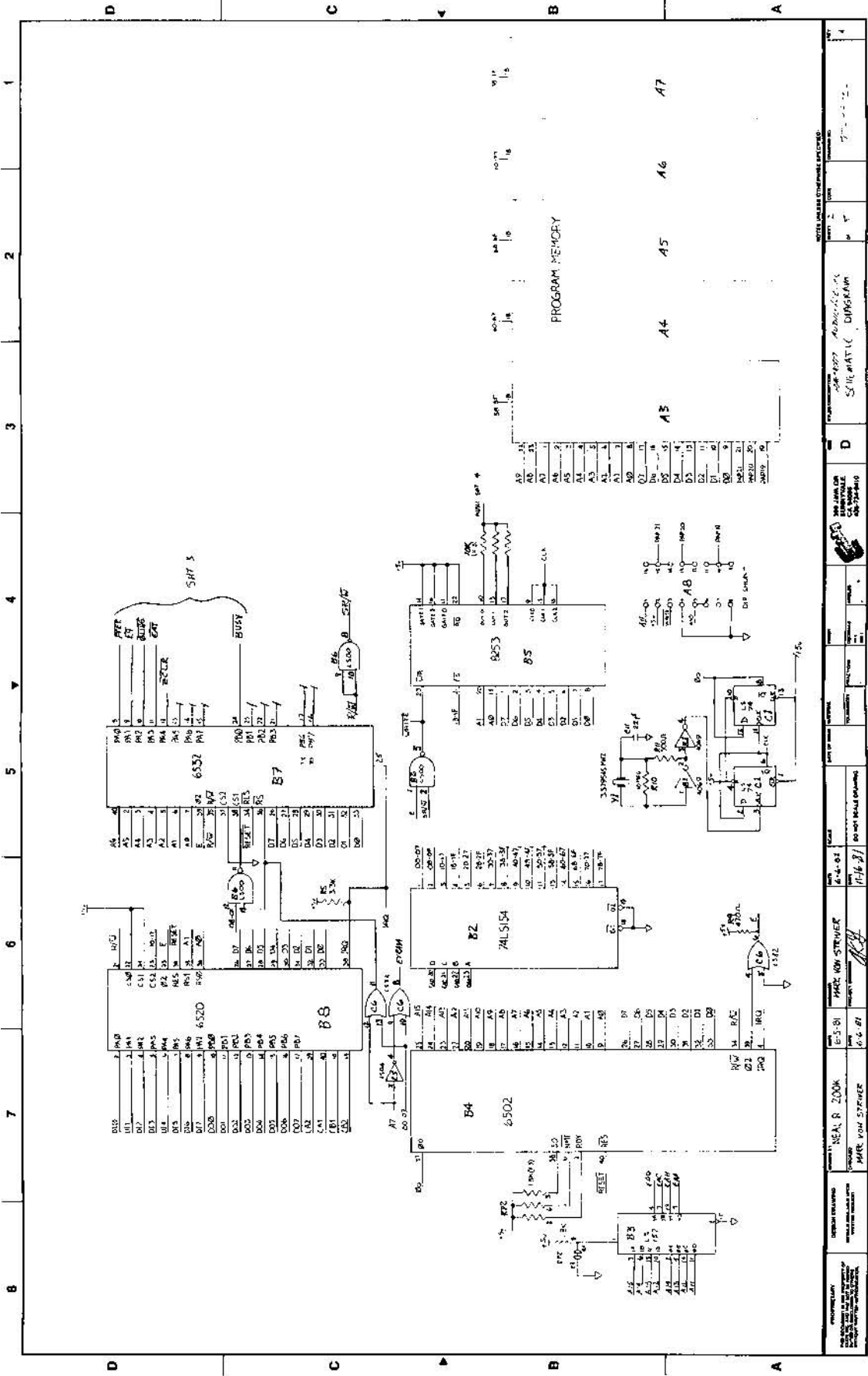
VIDEO
 OUTPUT
 TO
 AUDIO LOGIC PCB

1	SCLOCK
2	CSUB
3	FSYNC
4	SSCLK
5	MSYNC
6	LSYNC
7	MSUB
8	SUB
9	MA9
10	SJMS
11	MSUB
12	MS
13	MS
14	MS

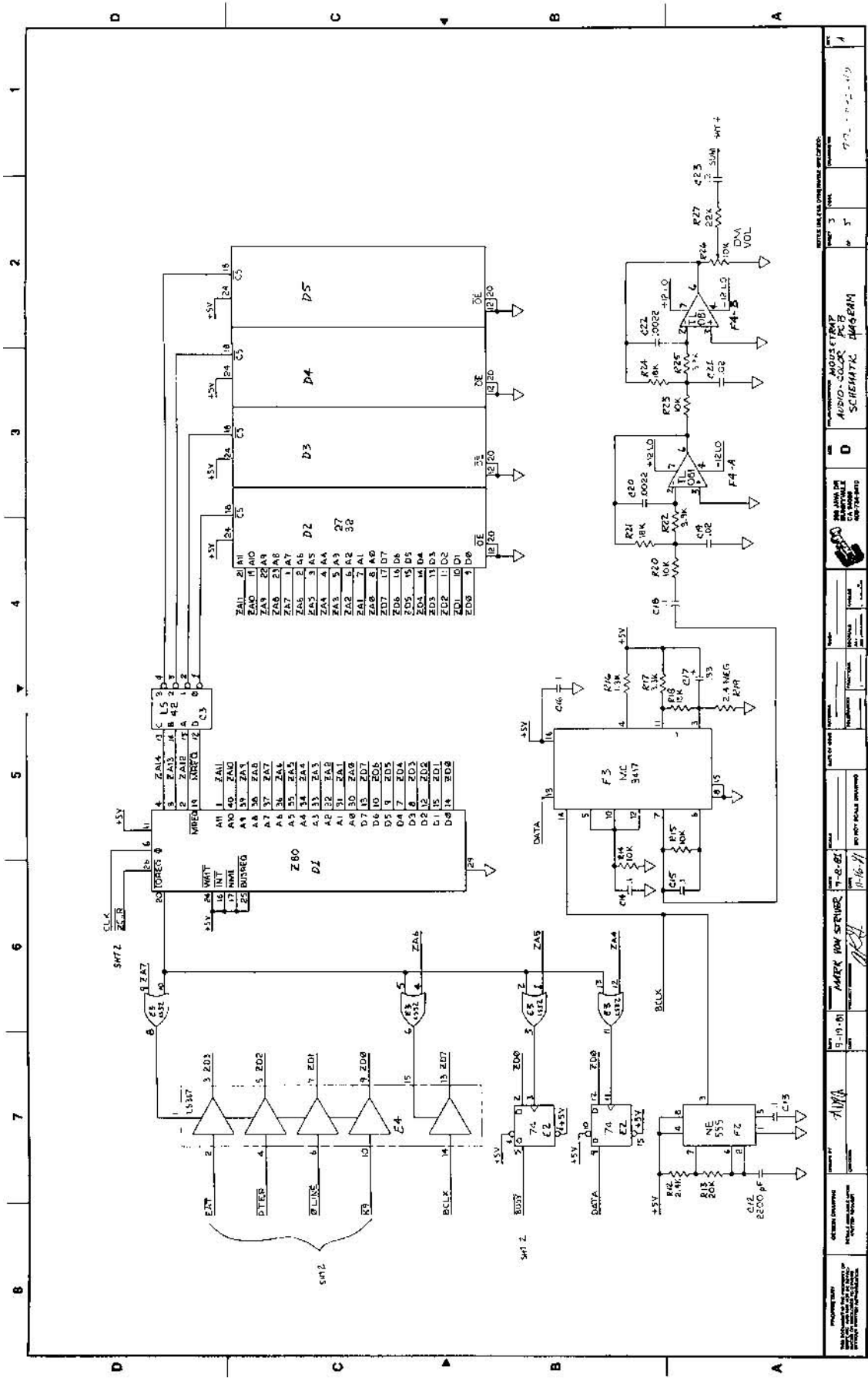




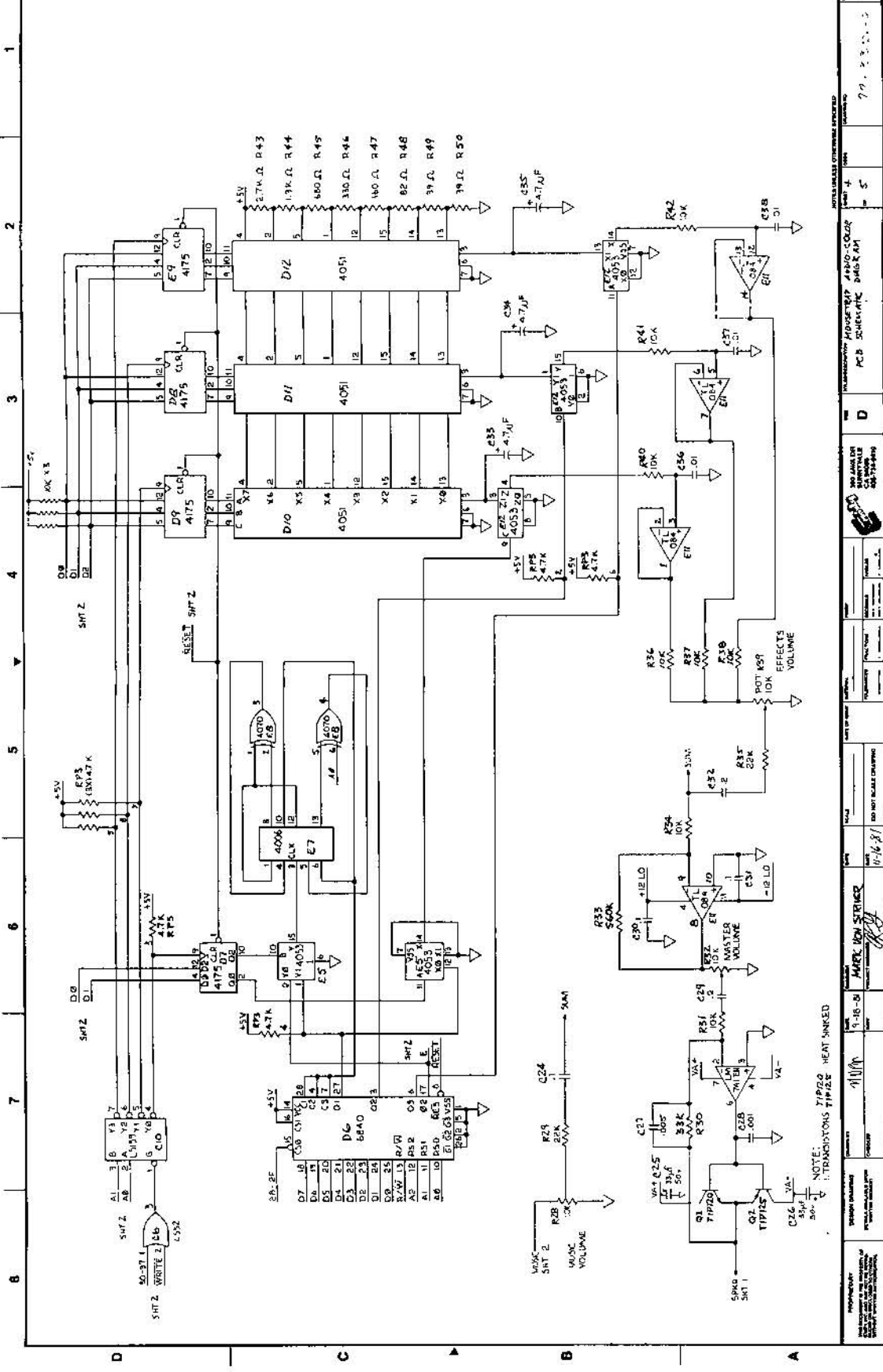
REV	DATE	BY	APP'D
17-10-81	11-11-81		
GAME LOGIC PCB		I/O PORTS	
6502		8088	
REV 17-10-81		11-11-81	



1	2	3	4	5	6	7	8							
D C B A							1	2	3	4	5	6	7	8
D C B A							PROGRAM MEMORY A3 A4 A5 A6 A7		SHT 3 A3 A4 A5 A6 A7		TITLE: SCHEMATIC DRAWING DRAWN BY: [Signature] CHECKED BY: [Signature] DATE: 11/16/61 REVISION: 1			
D C B A							REVISIONS NO. 1 DATE: 11/16/61 BY: [Signature]		APPROVED BY: [Signature] DATE: 11/16/61		PROJECT: [Blank] SHEET: 1 OF 1			



PROJECT NO.	9-10-81	DATE	11-16-81
DESIGNED BY	MARK W. STEUBER	IN-CHARGE	MARK W. STEUBER
CHECKED BY		DATE	
APPROVED BY		DATE	
AUDIO CASSETTE DECK SCHEMATIC DIAGRAM			
REV.	1	DATE	11-16-81
REV.	2	DATE	
REV.	3	DATE	
REV.	4	DATE	
REV.	5	DATE	
REV.	6	DATE	
REV.	7	DATE	
REV.	8	DATE	



1 2 3 4 5 6 7 8

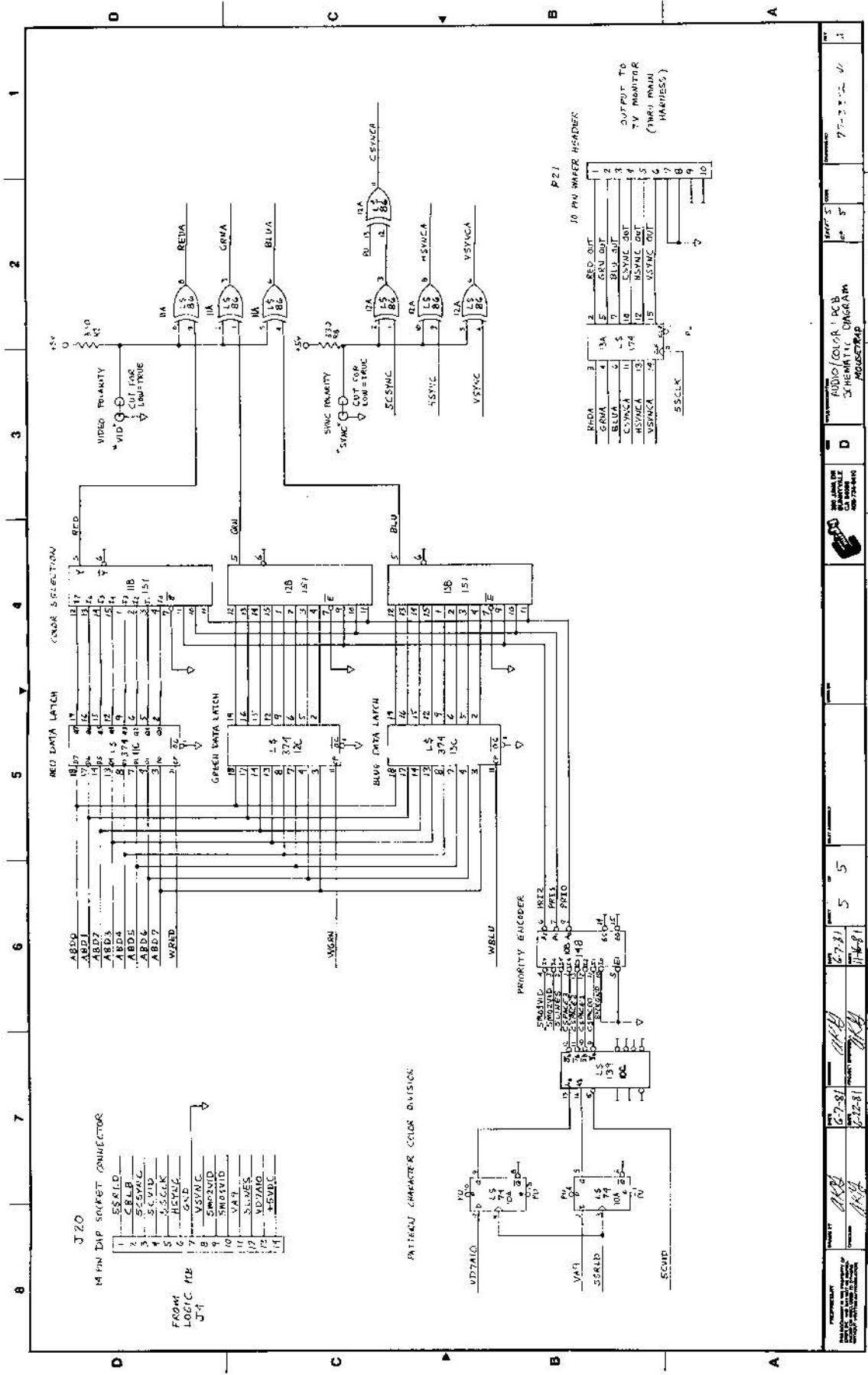
79, 2, 20, 1-2

D

PCB QUANTITATIVE DIAGRAM

PROFESSOR	MARK L. STERNER	DATE: 11-16-81	DO NOT SCALE DIMENSIONS
DESIGNED BY	MARK L. STERNER	DATE: 11-16-81	DO NOT SCALE DIMENSIONS
CHECKED BY	MARK L. STERNER	DATE: 11-16-81	DO NOT SCALE DIMENSIONS
DATE OF ORDER	MARK L. STERNER	DATE: 11-16-81	DO NOT SCALE DIMENSIONS
CUSTOMER NAME	MARK L. STERNER	DATE: 11-16-81	DO NOT SCALE DIMENSIONS
ADDRESS	MARK L. STERNER	DATE: 11-16-81	DO NOT SCALE DIMENSIONS
CITY	MARK L. STERNER	DATE: 11-16-81	DO NOT SCALE DIMENSIONS
STATE	MARK L. STERNER	DATE: 11-16-81	DO NOT SCALE DIMENSIONS
COUNTRY	MARK L. STERNER	DATE: 11-16-81	DO NOT SCALE DIMENSIONS

79, 2, 20, 1-2



J20
14 PIN DIP SOCKET CONNECTOR

FROM LOGIC PCB
J-1

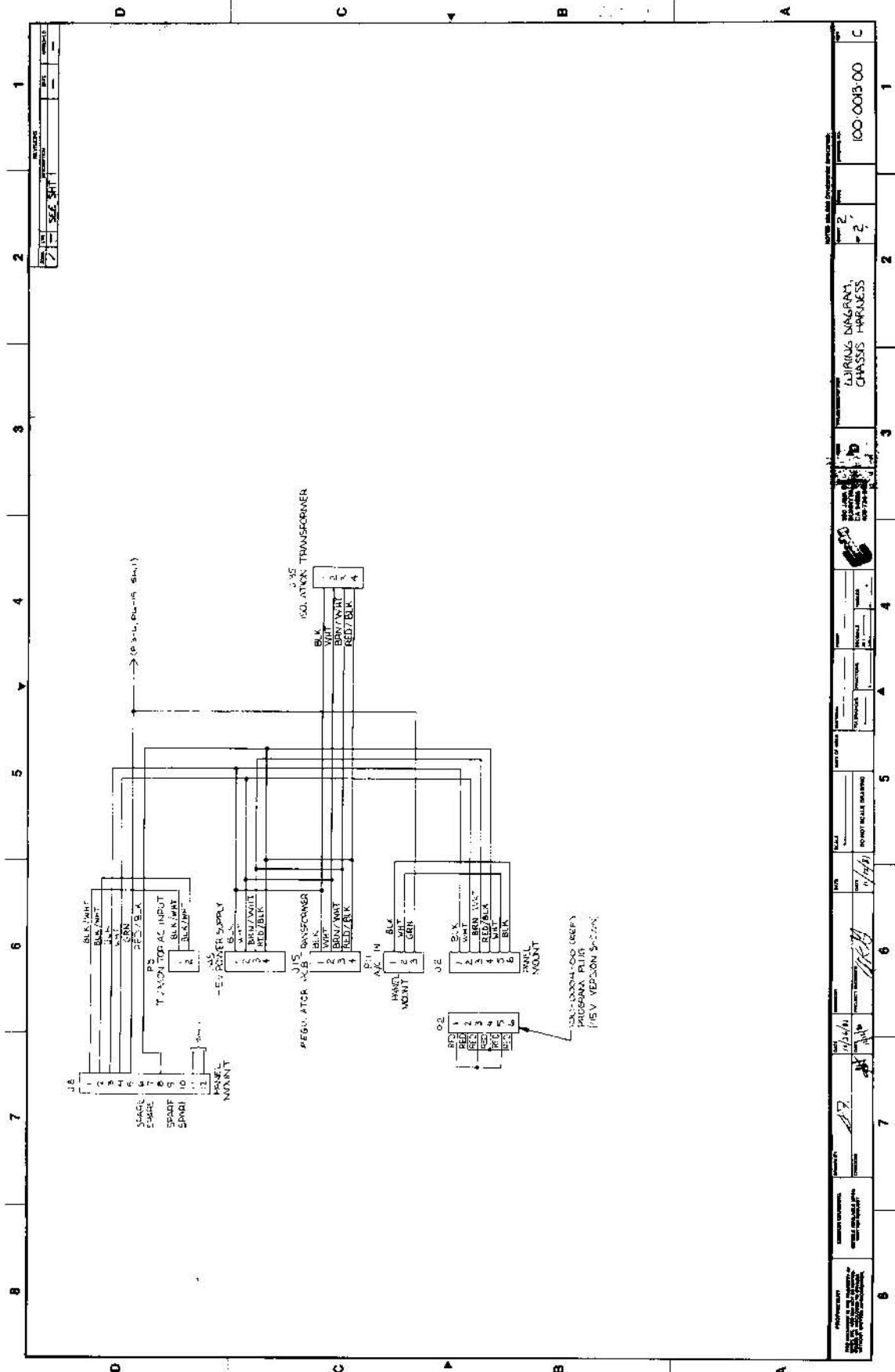
PRIORITY ENCODER COLOR DIVISION

PRIORITY ENCODER

P21
10 PIN HEADER

OUTPUT TO TV MONITOR (THRU MAIN HARNESS)

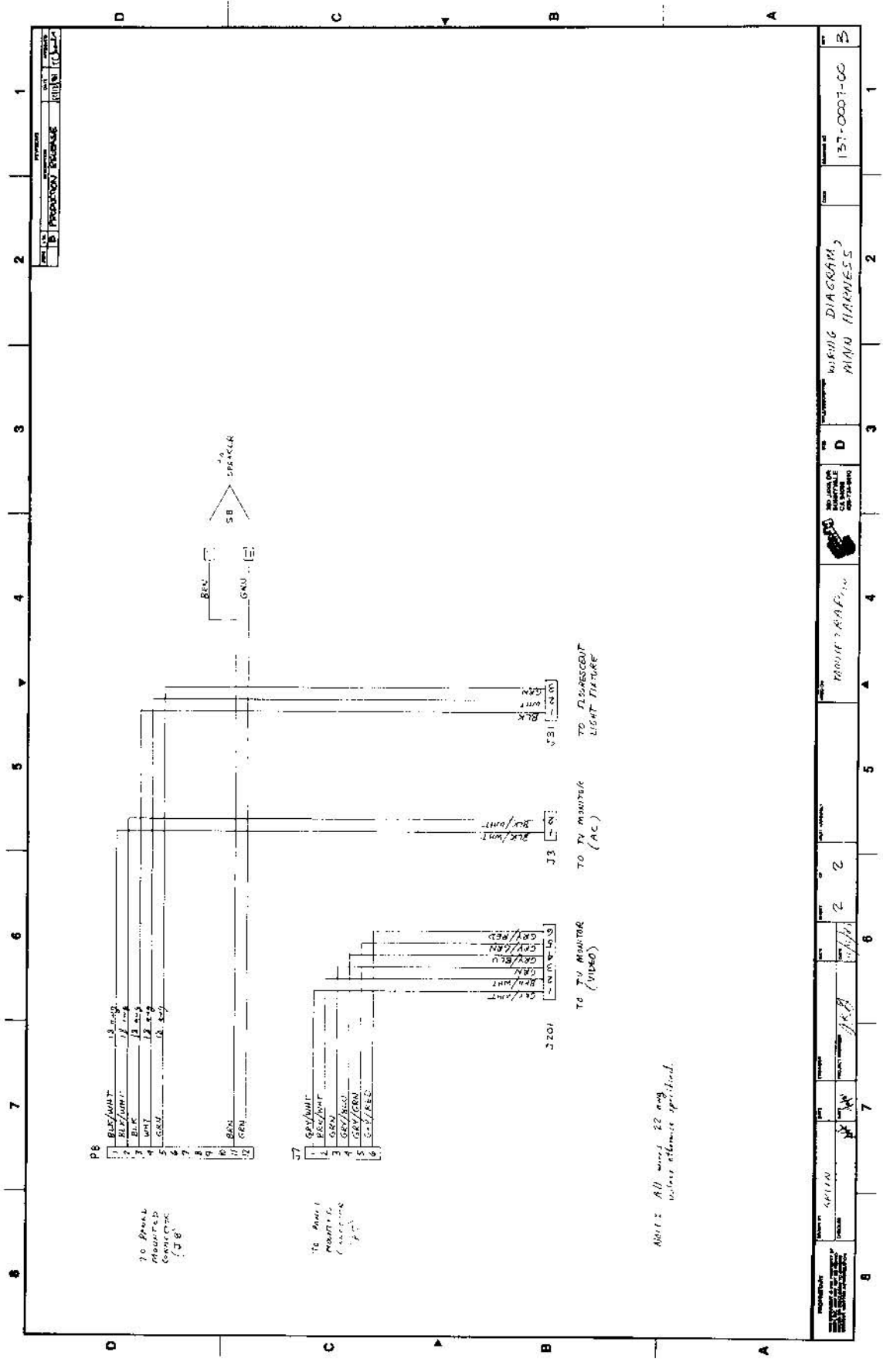
REV	1
DATE	7-7-81
DESIGNED BY	AKB
CHECKED BY	AKB
APPROVED BY	AKB
DATE	7-7-81
REV	5
DESCRIPTION	AUDIO/COLOR PCB SCHEMATIC DIAGRAM
PROJECT	HOUSETRAP
REV	5
DATE	7-7-81
DESIGNED BY	AKB
CHECKED BY	AKB
APPROVED BY	AKB
DATE	7-7-81



REV. NO.	DATE	BY	CHKD.
1	11/14/41	W. J. H.	
2			
3			
4			
5			
6			
7			
8			

REV. NO.	DATE	BY	CHKD.
1	11/14/41	W. J. H.	
2			
3			
4			
5			
6			
7			
8			

100-0003-00



REV. 1	DATE	BY
1		
B PRODUCTION PACKAGE		
REV. 1	DATE	BY
1		

P6

1	BLK/WHT	12 AWG
2	BLK/WHT	12 AWG
3	BLK	12 AWG
4	WHT	12 AWG
5	GRN	12 AWG
6	GRN	12 AWG
7	GRN	12 AWG
8	GRN	12 AWG

J7

1	GRN/WHT
2	BLK/WHT
3	GRN
4	GRN/BLU
5	GRN/GRN
6	GRN/GRN

J33

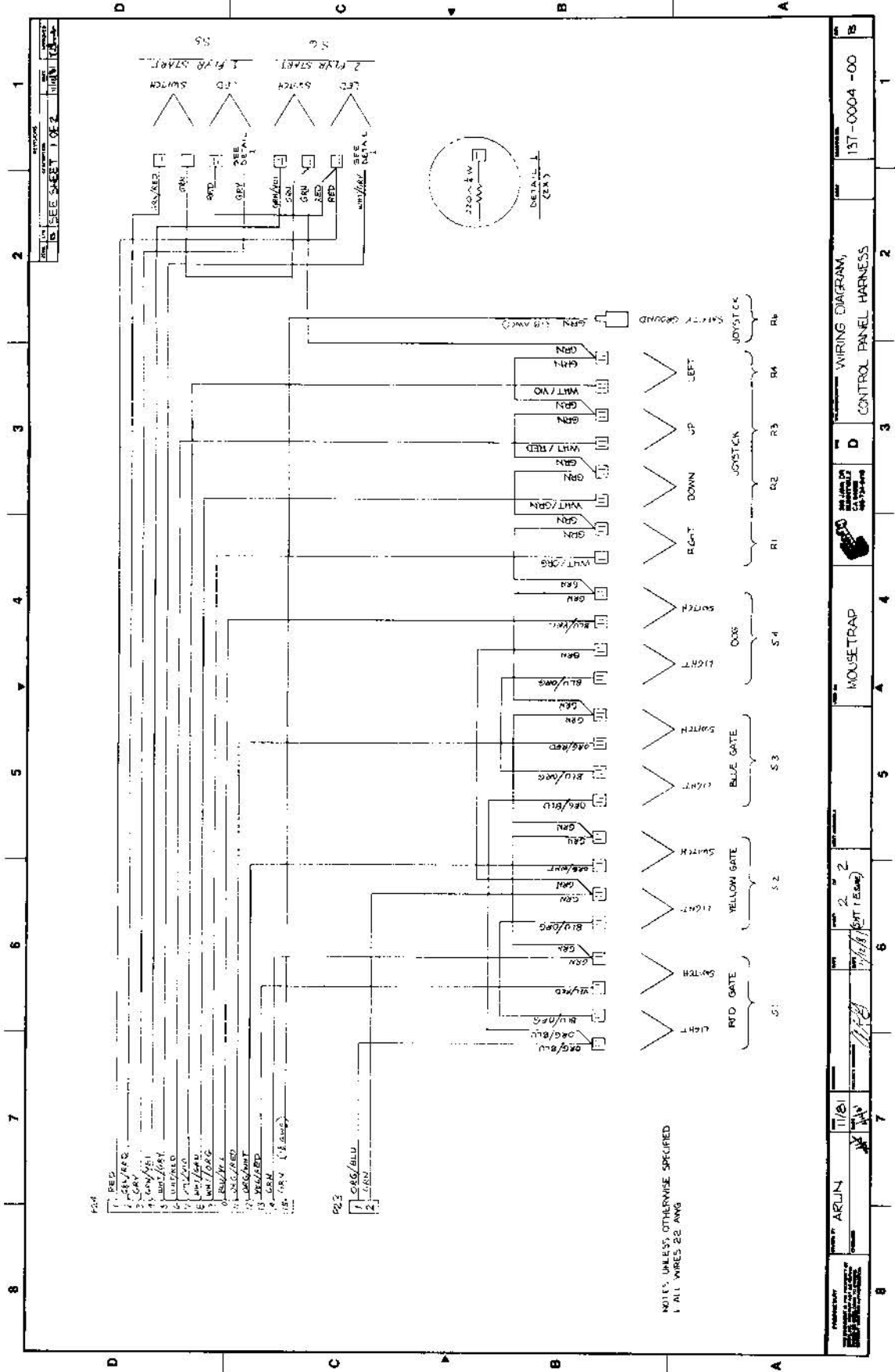
1	GRN/WHT
2	GRN
3	GRN
4	GRN
5	GRN
6	GRN

J31

1	GRN
2	GRN
3	GRN
4	GRN
5	GRN
6	GRN

NOTE: All wires 22 awg unless otherwise specified.

REV. 1	DATE	BY	REV. 1	DATE	BY
1			1		
WIRING DIAGRAM			137-0007-00		
D			D		
MAIN HARNESS			MAIN HARNESS		



PROPERTY OF ARJUN

DATE: 11/18/78

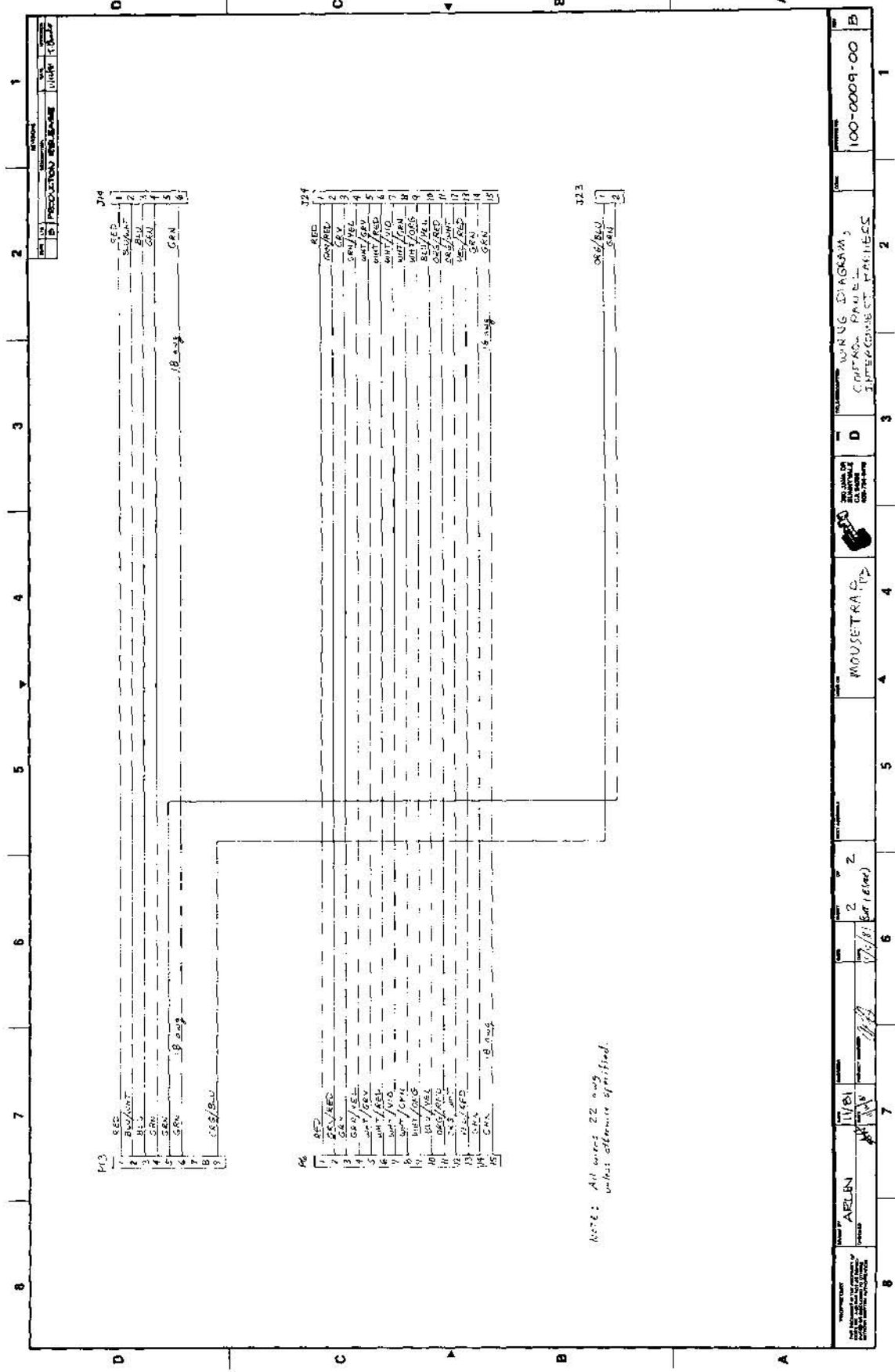
REV: 2 OF 2

REV: 11/18/78 (PITTING)

137-0004-00

WIRING DIAGRAM, CONTROL PANEL HARNESS

MOUSETRAP



M13

1	SEC	RED
2	BLK/WHIT	BLK/WHIT
3	BLK	BLK
4	GRN	GRN
5	GRN	GRN
6	GRN	GRN
7	GRN	GRN
8	GRN	GRN
9	GRN	GRN
10	GRN	GRN
11	GRN	GRN
12	GRN	GRN
13	GRN	GRN
14	GRN	GRN
15	GRN	GRN
16	GRN	GRN
17	GRN	GRN
18	GRN	GRN
19	GRN	GRN
20	GRN	GRN
21	GRN	GRN
22	GRN	GRN
23	GRN	GRN
24	GRN	GRN
25	GRN	GRN

M14

1	SEC	RED
2	BLK/WHIT	BLK/WHIT
3	BLK	BLK
4	GRN	GRN
5	GRN	GRN
6	GRN	GRN
7	GRN	GRN
8	GRN	GRN
9	GRN	GRN
10	GRN	GRN
11	GRN	GRN
12	GRN	GRN
13	GRN	GRN
14	GRN	GRN
15	GRN	GRN
16	GRN	GRN
17	GRN	GRN
18	GRN	GRN
19	GRN	GRN
20	GRN	GRN
21	GRN	GRN
22	GRN	GRN
23	GRN	GRN
24	GRN	GRN
25	GRN	GRN

M15

1	SEC	RED
2	BLK/WHIT	BLK/WHIT
3	BLK	BLK
4	GRN	GRN
5	GRN	GRN
6	GRN	GRN
7	GRN	GRN
8	GRN	GRN
9	GRN	GRN
10	GRN	GRN
11	GRN	GRN
12	GRN	GRN
13	GRN	GRN
14	GRN	GRN
15	GRN	GRN
16	GRN	GRN
17	GRN	GRN
18	GRN	GRN
19	GRN	GRN
20	GRN	GRN
21	GRN	GRN
22	GRN	GRN
23	GRN	GRN
24	GRN	GRN
25	GRN	GRN

Note: All areas 22 sq. ft. unless otherwise specified.

PROJECT NO.	100-0009-00
DATE	12/1/81
BY	AWB
CHECKED	AWB
SCALE	AS SHOWN
TITLE	WORKING DIAGRAM
PROJECT	CORPORATE PLANNING
LOCATION	INTERCOMBUSTIBLES
ROOM NO.	
REVISIONS	